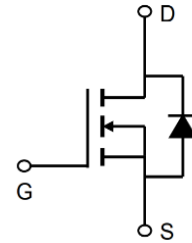




Description

These N-Channel enhancement mode power field effect transistors are using shielded gate trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.



General Features

$V_{DS} = 150V$ $I_D = 190A$

$R_{DS(ON)} < 7.5m\Omega$ @ $V_{GS} = 10V$ (Type: **6.6m Ω**)

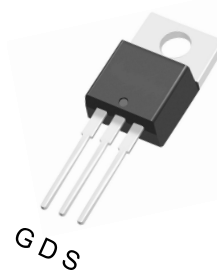
Application

DC/DC Converter

LED Backlighting

Power Management Switches

TO-220



Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V	190	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V	140	A
IDM	Pulsed Drain Current	550	A
EAS	Single Pulse Avalanche Energy	506	mJ
IAS	Avalanche Current	53.4	A
P _D @T _C =25°C	Total Power Dissipation ⁴	210	W
TSTG	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C
R _{θJA}	Thermal Resistance Junction-Ambient	0.84	°C/W
R _{θJC}	Thermal Resistance Junction-Case	62	°C/W



Electrical Characteristics (T_c=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
VDSS	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	150	-	-	V
IGSS	Gate-body Leakage current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
IDSS	Zero Gate Voltage Drain Current T _J =25°C	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
IDSS	Zero Gate Voltage Drain Current T _J =100°C		-	-	100	
VGS(th)	Gate-Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.0	2.9	4.0	V
RDS(on)	Drain-Source on-Resistance ²	V _{GS} = 10V, I _D = 20A	-	6.6	7.5	mΩ
Ciss	Input Capacitance	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz	-	5240	-	pF
Coss	Output Capacitance		-	412	-	
Crss	Reverse Transfer Capacitance		-	10	-	
R _g	Gate Resistance	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz	-	1.7	-	Ω
Q _g	Total Gate Charge	V _{GS} = 10V, V _{DS} = 50V, I _D = 20A	-	18	-	nC
Q _{gs}	Gate-Source Charge		-	10	-	
Q _{gd}	Gate-Drain Charge		-	72	-	
td(on)	Turn-on Delay Time	V _{GS} = 10V, V _{DS} = 50V, R _G = 3Ω, I _D = 20A	-	22	-	ns
t _r	Rise Time		-	115	-	
td(off)	Turn-off Delay Time		-	44	-	
t _f	Fall Time		-	105	-	
VSD	Diode Forward Voltage ²	I _F = 20A, V _{GS} = 0V	-	-	1.2	V
IS	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	-	-	190	A
trr	Body Diode Reverse Recovery Time	I _F = 20A, di/dt=100A/μs	-	45	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	12	-	nC

Notes:

- 1、 The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3、 The EAS data shows Max. rating . The test condition is V_{DD}=50V, V_{GS}=10V, L=0.5mH, I_{AS}=45A
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

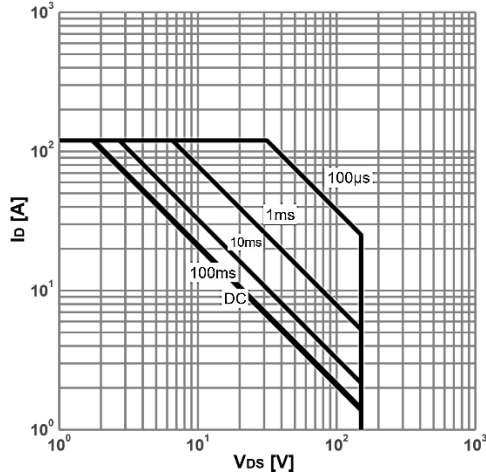


Figure 1. Power dissipation

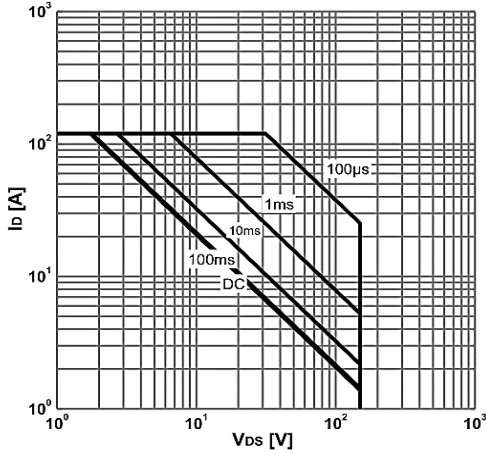


Figure 3. Safe operating area

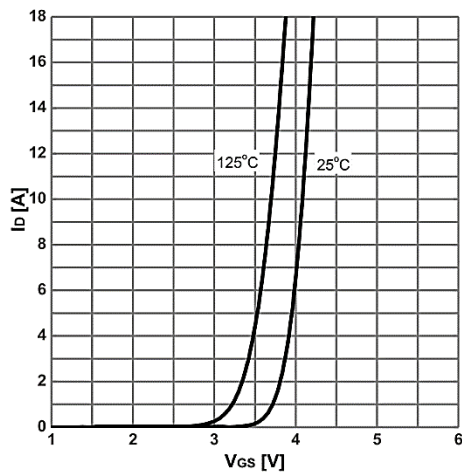


Figure 5. Typ. transfer characteristics

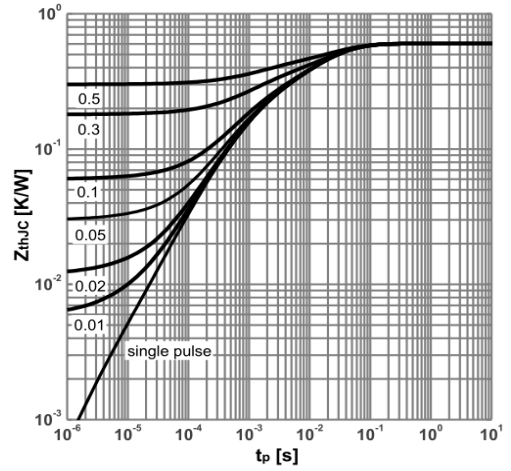


Figure 2. Max. transient thermal impedance

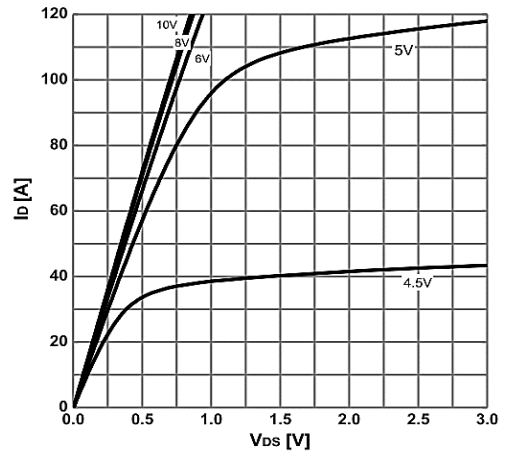


Figure 4. Typ. output characteristics

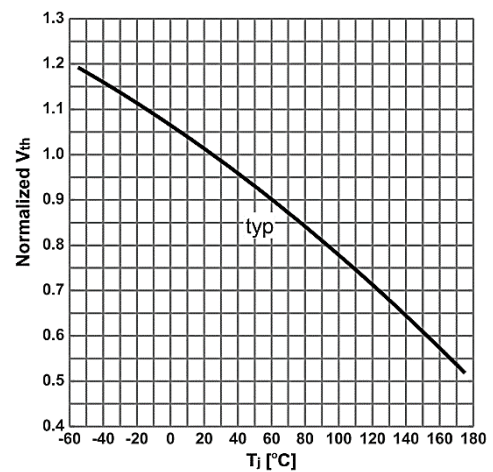


Figure 6. Gate threshold voltage vs. Junction Temperature

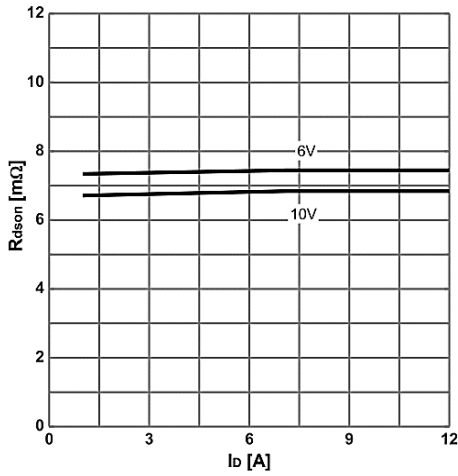


Figure 7. On-state resistance vs. Drain current

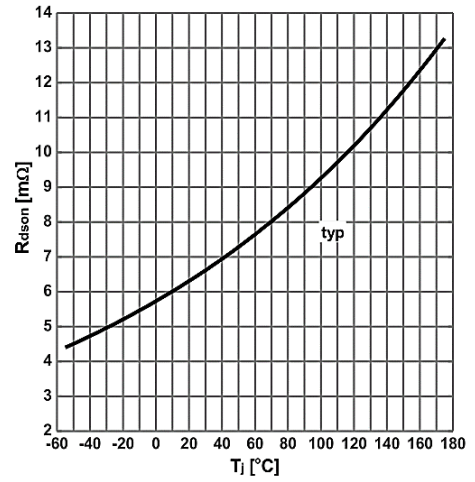


Figure 8. On-state resistance vs. Junction temperature

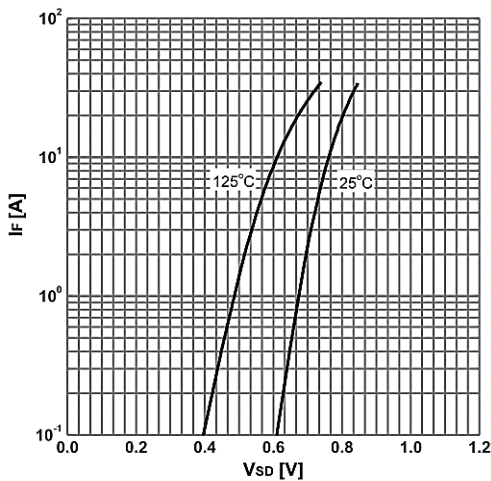


Figure 9. Forward characteristics of reverse diode

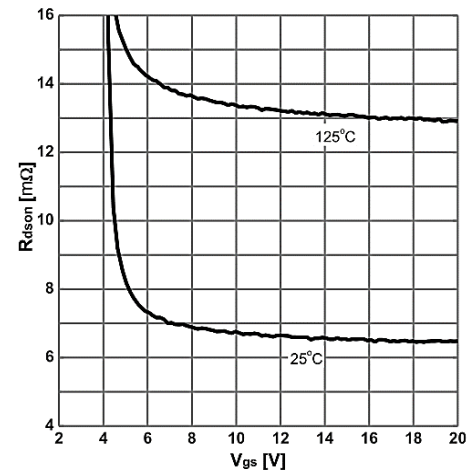


Figure 10. On-state resistance vs. Vgs characteristics

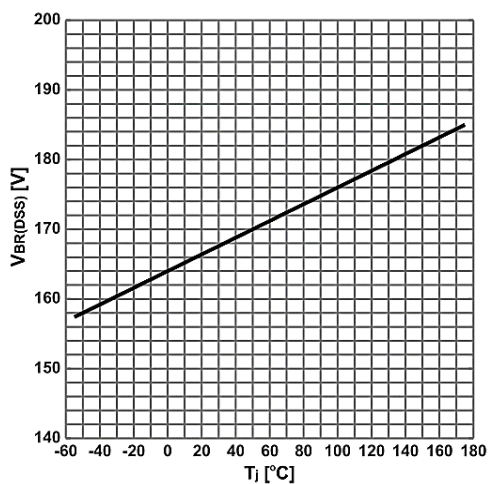


Figure 10: Breakdown Voltage Variation vs. Temperature

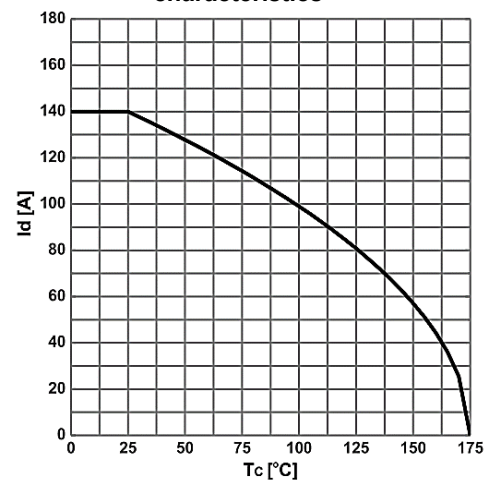
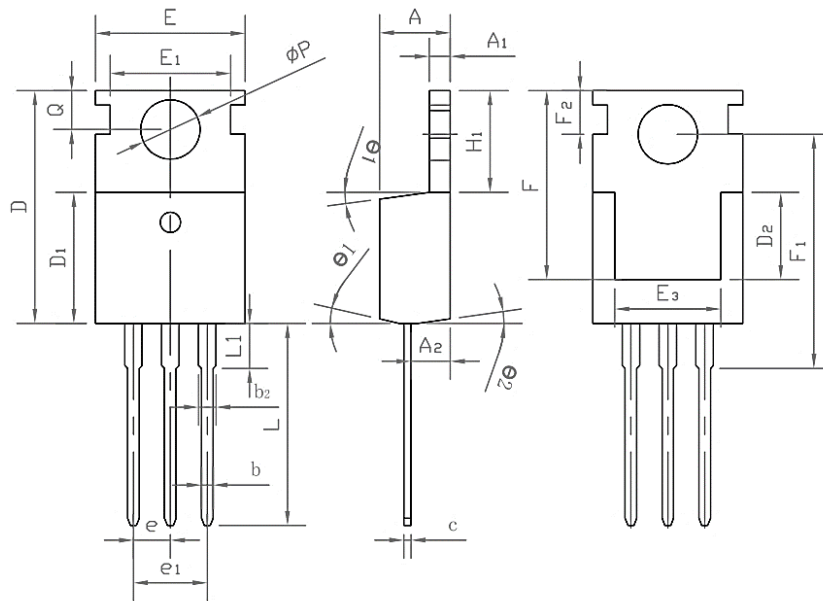


Figure 11: Maximum Drain Current



Package Mechanical Data-TO-220-3L



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.27	4.57	4.87
A1	1.15	1.30	1.45
A2	2.10	2.40	2.70
b	0.70	0.80	1.00
b2	1.17	1.27	1.50
D	0.40	0.50	0.65
D1	8.80	9.10	9.40
D2	5.70	6.70	7.00
E	9.70	10.00	10.30
E1	-	8.70	-
E2	9.63	10.00	10.35
E3	7.00	8.00	8.40
e		0.37	
e1		0.10	
H1	6.00	6.50	6.85
L	12.75	13.50	13.90
L1	-	3.10	3.40
Φp	3.45	3.60	3.75
Q	2.60	2.80	3.00
θ1	4°	7°	10°
θ2	0°	3°	6°
F	13.30	13.50	13.70
F1	15.50	15.90	16.30
F2	2.80	3.00	3.20



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