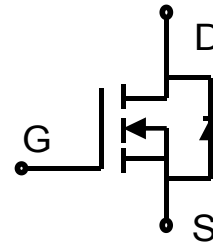




### Description

WENLAI Power MOSFET is fabricated using **advanced super junction** technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.



N-Channel MOSFET

### Features

- ◆ Ultra low  $R_{DS(on)}$
- ◆ Ultra low gate charge (typ.  $Q_g = 19.9nC$ )
- ◆ 100% UIS tested
- ◆ RoHS compliant

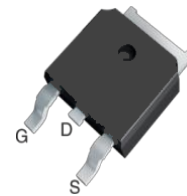
### Applications

- ◆ Power factor correction (PFC).
- ◆ Switched mode power supplies (SMPS).
- ◆ Uninterruptible power supply (UPS).

### Product Summary

$V_{DS} @ T_{j,max}$	700V
$R_{DS(on),max}$	0.38Ω
$I_{DM}$	33A
$Q_{g,typ}$	19.9 nC

### Pin Configuration



TO-252

### Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	650	V
Continuous drain current ( $T_C = 25^\circ C$ ) ( $T_C = 100^\circ C$ )	$I_D$	11	A
		7	A
Pulsed drain current <sup>1)</sup>	$I_{DM}$	33	A
Gate-Source voltage	$V_{GSS}$	$\pm 30$	V
Avalanche energy, single pulse <sup>2)</sup>	$E_{AS}$	218	mJ
Power Dissipation	$P_D$	113	W
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ C$
Continuous diode forward current	$I_S$	11	A
Diode pulse current	$I_{S,pulse}$	33	A

### Thermal Characteristics

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.1	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient <sup>3)</sup>	$R_{\theta JA}$	120	$^\circ C/W$

### Package Marking and Ordering Information

Device	Device Package	Marking	Units/Reel
WLU380R650GM	TO-252	WLU380R650GM	2500



### Electrical Characteristics T<sub>c</sub> = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static characteristics</b>						
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0 V, I <sub>D</sub> =0.25 mA	650	-	-	V
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =0.25mA	2.5	3.0	4.5	V
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> =650 V, V <sub>GS</sub> =0 V, T <sub>J</sub> = 25°C	-	-	1	μA
Gate leakage current, Forward	I <sub>GSSF</sub>	V <sub>GS</sub> =30 V, V <sub>DS</sub> =0 V	-	-	100	nA
Gate leakage current, Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> =-30 V, V <sub>DS</sub> =0 V	-	-	-100	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =5.5 A T <sub>J</sub> = 25°C T <sub>J</sub> = 150°C	-	-	-	Ω
			-	0.32	0.38	
			-	0.8	-	
Gate resistance	R <sub>G</sub>	f=1 MHz, open drain	-	13	-	Ω
<b>Dynamic characteristics</b>						
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, f = 250 kHz	-	740	-	pF
Output capacitance	C <sub>oss</sub>		-	32.7	-	
Reverse transfer capacitance	C <sub>rss</sub>		-	0.91	-	
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> = 400V, I <sub>D</sub> = 5.5A R <sub>G</sub> = 10Ω, V <sub>GS</sub> =15V	-	20.4	-	ns
Rise time	t <sub>r</sub>		-	31.2	-	
Turn-off delay time	t <sub>d(off)</sub>		-	83.8	-	
Fall time	t <sub>f</sub>		-	13.7	-	
<b>Gate charge characteristics</b>						
Gate to source charge	Q <sub>gs</sub>	V <sub>DD</sub> =520 V, I <sub>D</sub> =5.5A, V <sub>GS</sub> =0 to 10 V	-	3.6	-	nC
Gate to drain charge	Q <sub>gd</sub>		-	9.1	-	
Gate charge total	Q <sub>g</sub>		-	19.9	-	
Gate plateau voltage	V <sub>plateau</sub>		-	5	-	V
<b>Reverse diode characteristics</b>						
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0 V, I <sub>F</sub> =5.5A	-	-	1.1	V
Reverse recovery time	t <sub>rr</sub>	V <sub>R</sub> =400 V, I <sub>F</sub> =5.5A, dI <sub>F</sub> /dt=100 A/μs	-	260	-	ns
Reverse recovery charge	Q <sub>rr</sub>		-	2.3	-	μC
Peak reverse recovery current	I <sub>rrm</sub>		-	18	-	A

#### Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- I<sub>AS</sub> = 3.3A, L=40mH, V<sub>DD</sub> = 60V, Starting T<sub>J</sub>= 25°C.
- The value of R<sub>thJA</sub> is measured by placing the device in a still air box which is one cubic foot.



### Electrical Characteristics Diagrams

Figure 1. Typ. Output Characteristics

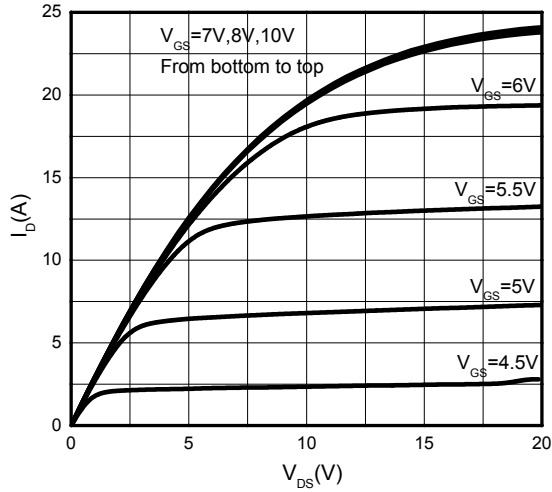


Figure 2. Transfer Characteristics

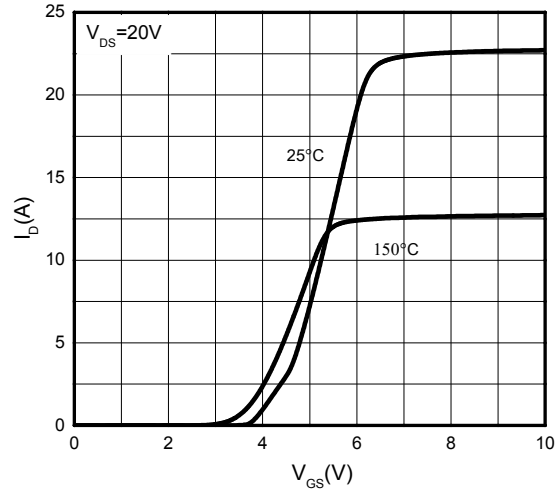


Figure 3. On-Resistance vs. Drain Current

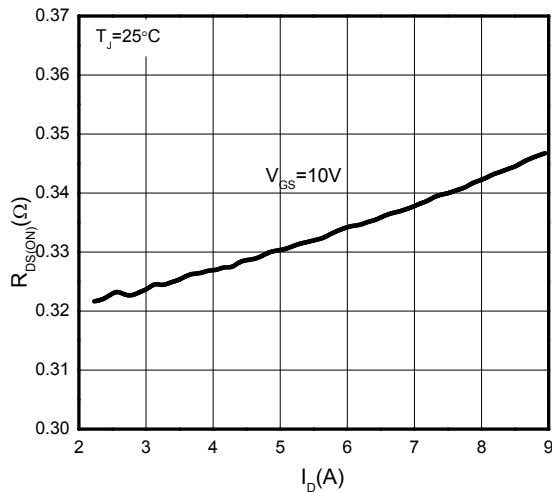


Figure 4. On-Resistance vs. Temperature

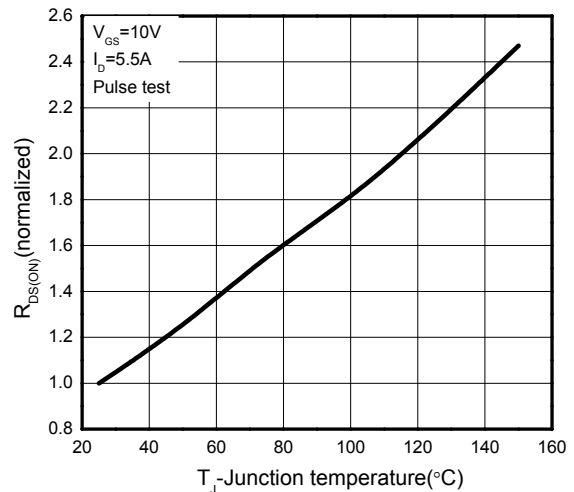


Figure 5. Breakdown Voltage vs. Temperature

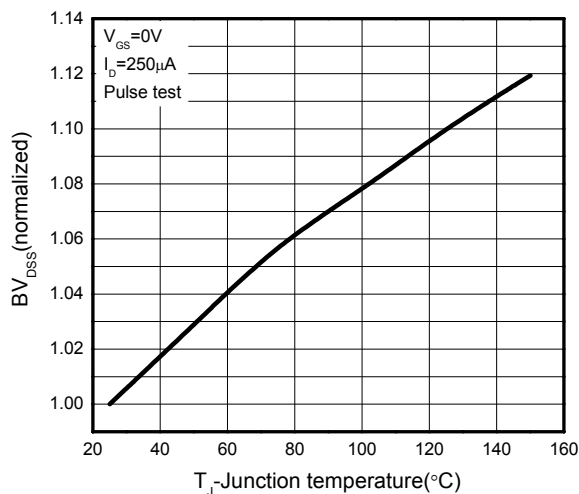


Figure 6. Threshold Voltage vs. Temperature

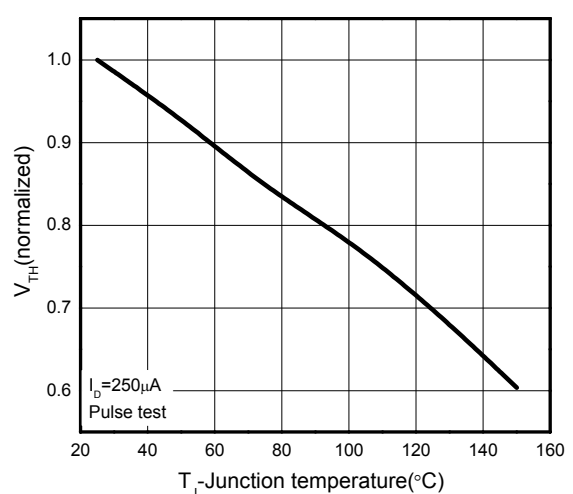




Figure 7. Body-Diode Characteristics

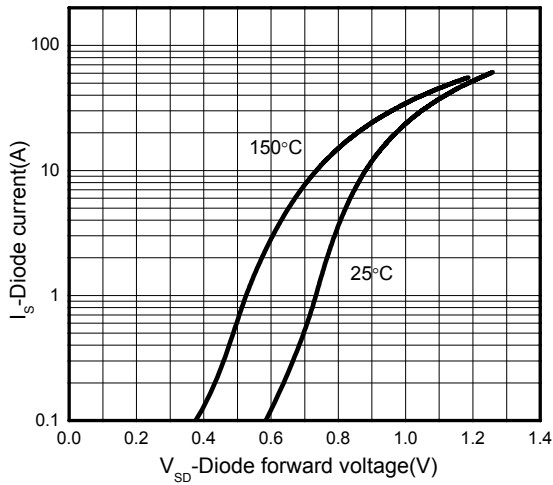


Figure 8. Capacitance Characteristics

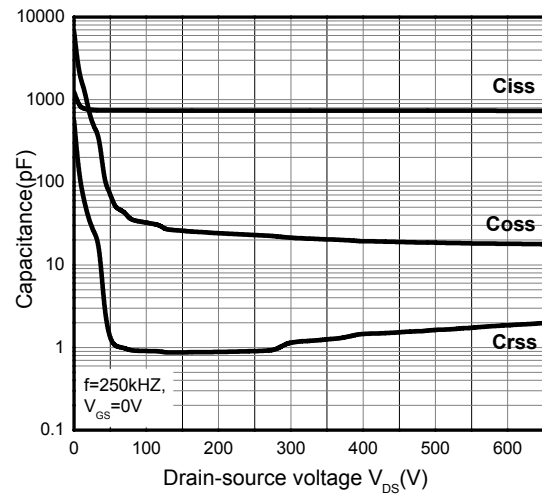


Figure 9. Gate Charge Characteristics

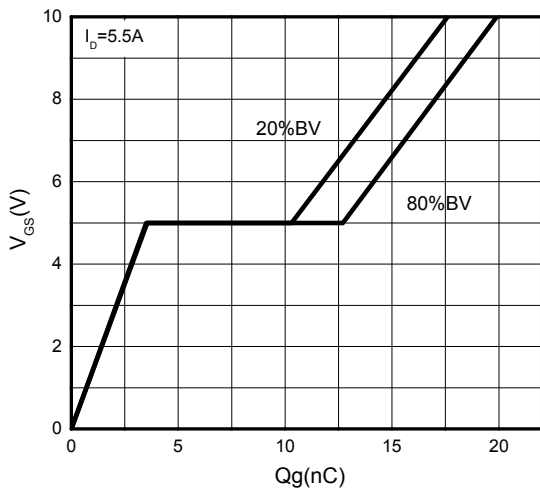


Figure 10. Drain Current Derating

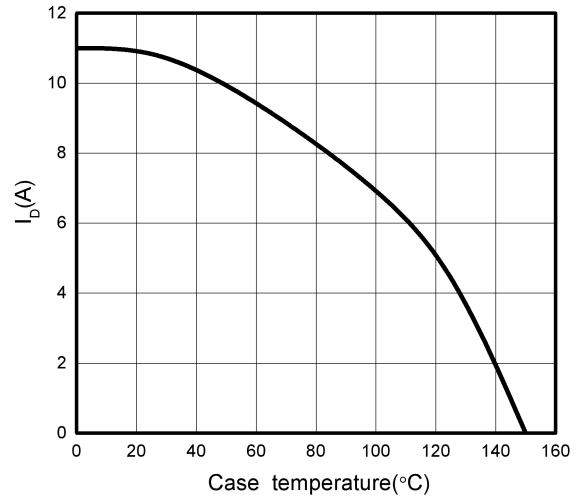


Figure 11. Power Dissipation vs. Temperature

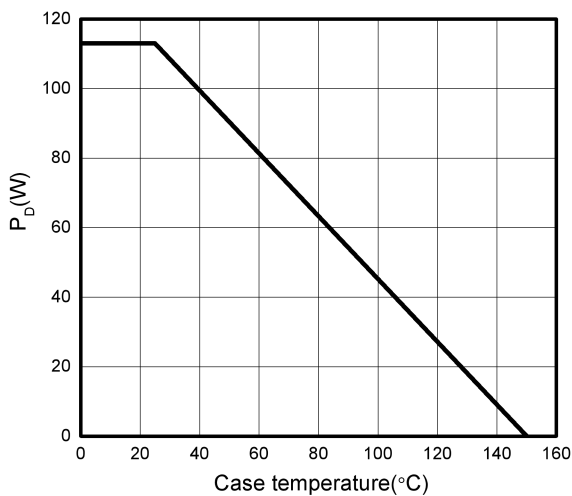


Figure 12: Safe Operating Area

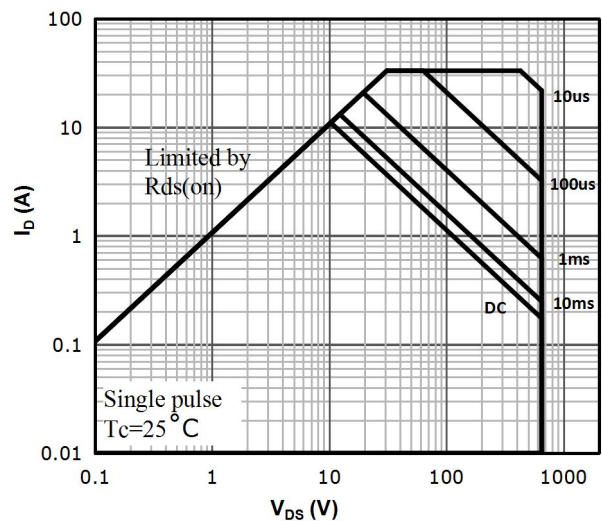
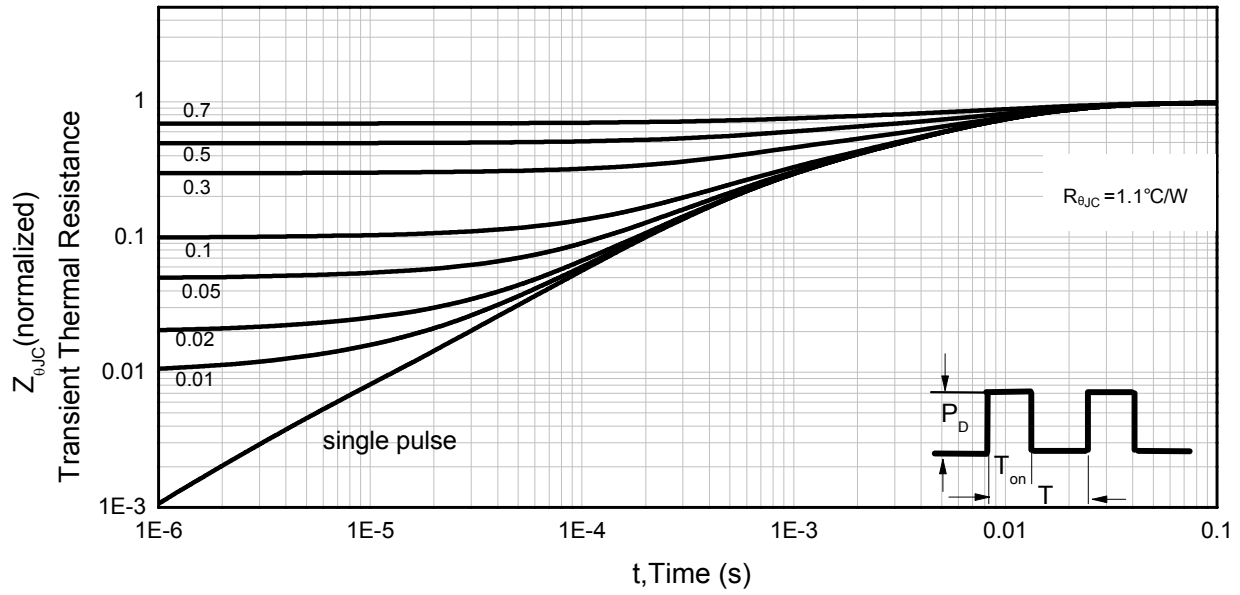




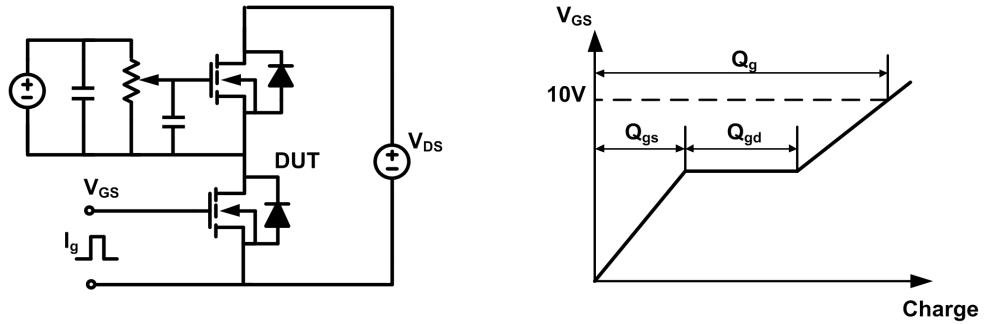
Figure 13. Normalized Maximum Transient Thermal Impedance (RthJC)



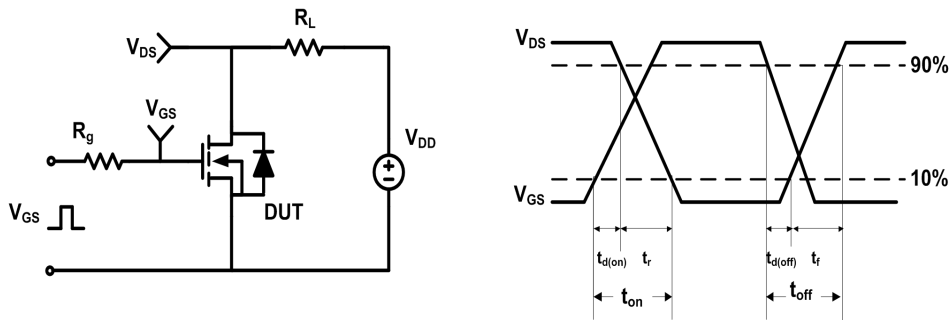


### Test Circuit & Waveforms

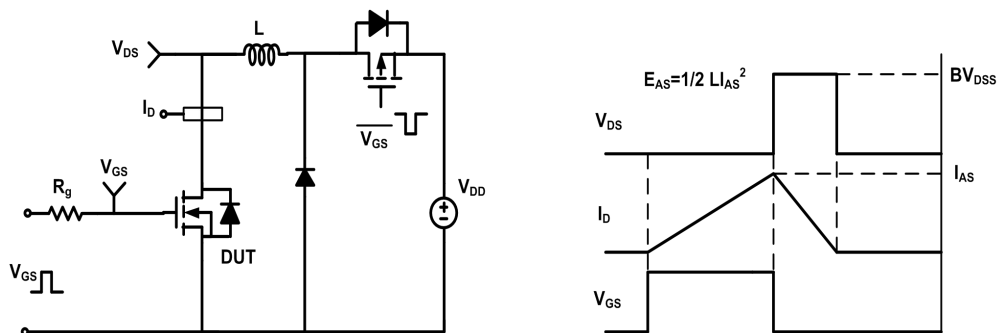
#### Gate Charge Test Circuit & Waveform



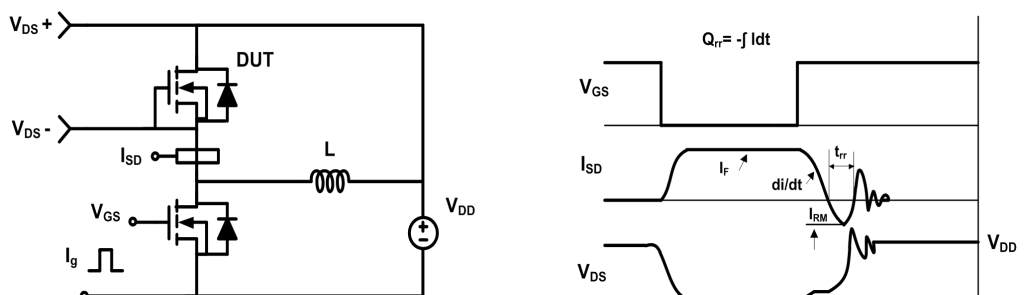
#### Resistive Switching Test Circuit & Waveform



#### Unclamped Inductive Switching (UIS) Test Circuit & Waveform

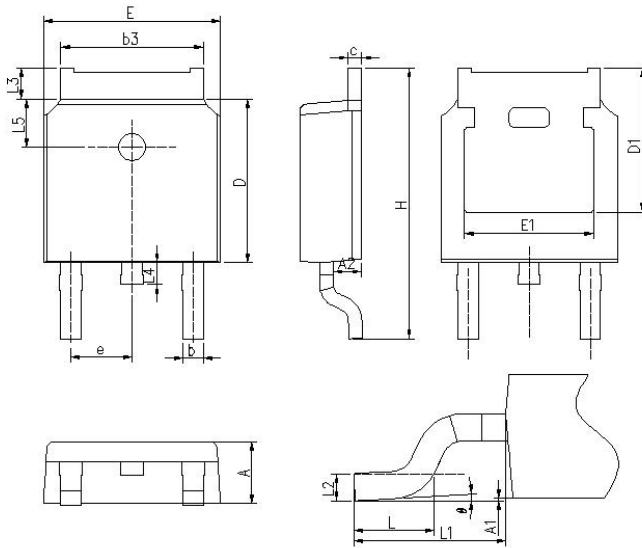


#### Diode Recovery Test Circuit & Waveform





### Mechanical Dimensions for TO-252



DIMENSIONS IN MILLIMETERS		
SYMBOL	MIN	MAX
A	2.18	2.4
A1	-	0.2
A2	0.9	1.17
b	0.65	0.9
b3	4.95	5.5
c	0.43	0.89
D	5.97	6.22
D1	5.21	-
E	6.35	6.8
E1	4.32	-
e	2.286BSC	
H	9.4	10.5
L	0.38	1.78
L1	2.90BSC	
L2	0.51BSC	
L3	0.88	1.28
L4	-	1.02
L5	1.65	1.95
θ	0°	10°



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