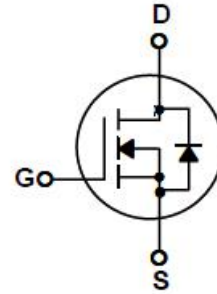




### General Description

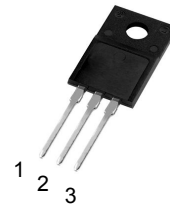
This Power MOSFET is produced by WENLAI using its own advanced planar stripe DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.



### Features

- 16A, 500V,  $R_{DS(on)} = 280m\Omega @ V_{GS} = 10V$
- Low gate charge ( typical 31nC)
- Low Crss ( typical 6.8pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

TO-220F Package



1. Gate 2. Drain 3. Source

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain-Source Voltage	500	V
$I_D$	Drain Current - Continuous (TC= 25°C) - Continuous (TC= 100°C)	16	A
		10.4*	A
$I_{DM}$	Drain Current - Pulsed (Note 1)	64*	A
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)	405	mJ
$I_{AR}$	Avalanche Current (Note 1)	16	A
$E_{AR}$	Repetitive Avalanche Energy (Note 1)	58	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	5	V/ns
$P_D$	Power Dissipation (TC = 25°C) - Derate above 25°C	29.8	W
		0.26	W/°C
$T_j, T_{stg}$	Operating and Storage Temperature Range	-55 to +150	°C
$T_L$	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	°C

\* Drain current limited by maximum junction temperature

### Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	3.92	°C/W
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	--	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	45.8	°C/W



### Electrical Characteristics TC = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	500			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250\ \mu\text{A}$ , Referenced to 25°C		0.51		V/°C
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 500\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 400\text{ V}, TC = 125^\circ\text{C}$			10	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$			100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$			-100	nA
<b>On Characteristics</b>						
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\ \mu\text{A}$	3.0		5.0	V
$R_{DS(On)}$	Drain-Source On-state Resistance	$V_{GS}=10\text{ V}, I_D=8\text{ A}, T_J=25^\circ\text{C}$		280	400	m $\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=40\text{ V}, I_D=8\text{ A}$ (Note 4)		16		S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{ V}, V_{GS}=0\text{ V}, f=1.0\text{ MHz}$		1670		pF
$C_{oss}$	Output Capacitance			247		pF
$C_{riss}$	Reverse Transfer Capacitance			6.8		pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn On Delay Time	$V_{DD}=250\text{ V}, I_D=16\text{ A}, R_G=25\ \Omega$ (Note 4, 5)		27		ns
$t_r$	Rising Time			45		ns
$t_{d(off)}$	Turn Off Delay Time			61		ns
$t_f$	Fall Time			38		ns
$Q_g$	Total Gate Charge	$V_{DS}=250\text{ V}, I_D=16\text{ A}, V_{GS}=10\text{ V}$ (Note 4, 5)		31		nC
$Q_{gs}$	Gate-Source Charge			13		nC
$Q_{gd}$	Gate-Drain Charge			9		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current				16	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current				64	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{ V}, I_S=16\text{ A}$			1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS}=0\text{ V}, I_S=16\text{ A}, di_F/dt=100\text{ A}/\mu\text{s}$ (Note 4)		342		ns
$Q_{rr}$	Reverse Recovery Charge			4.0		$\mu\text{C}$

**Notes:**

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. L = 3.2 mH, IAS = 16A, VDD = 50V, RG = 25 $\Omega$ , Starting TJ = 25°C
3. ISD ≤ 20A, di/dt ≤ 200A/ $\mu\text{s}$ , VDD ≤ BVDSS, Starting TJ = 25°C
4. Pulse Test : Pulse width ≤ 300 $\mu\text{s}$ , Duty cycle ≤ 2%
5. Essentially independent of operating temperature



### Typical Characteristics

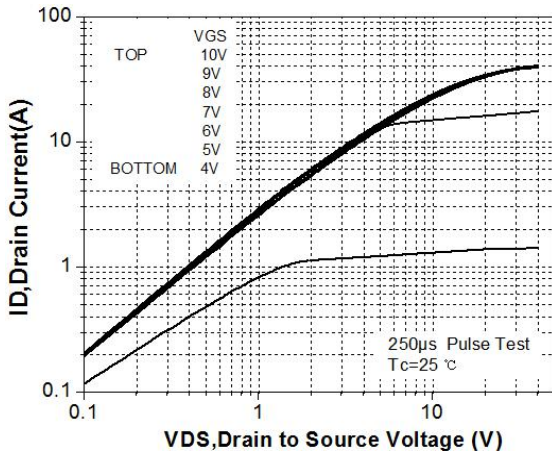


Figure 1. On-Region Characteristics

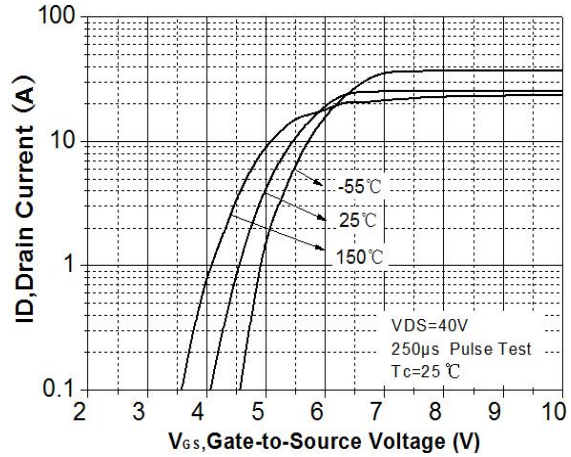


Figure 2. Transfer Characteristics

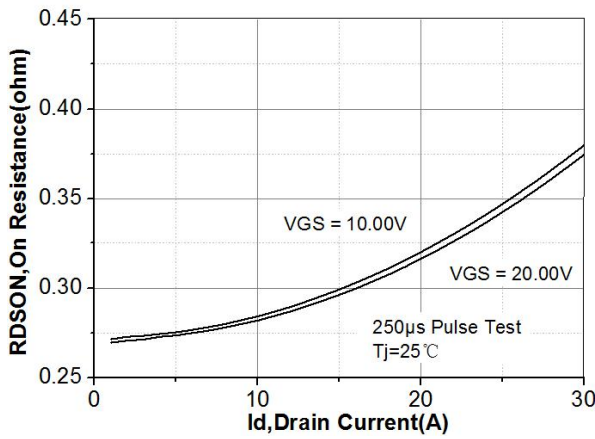


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

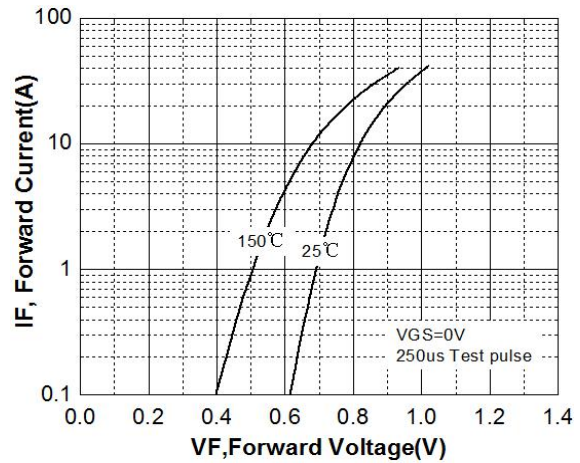


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

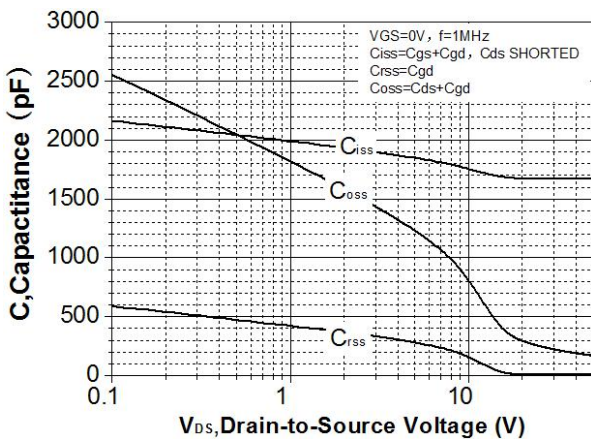


Figure 5. Capacitance Characteristics

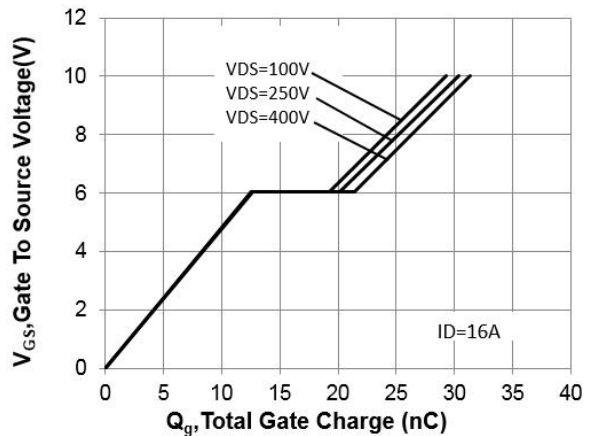


Figure 6. Gate Charge Characteristics



Typical Characteristics (Continued)

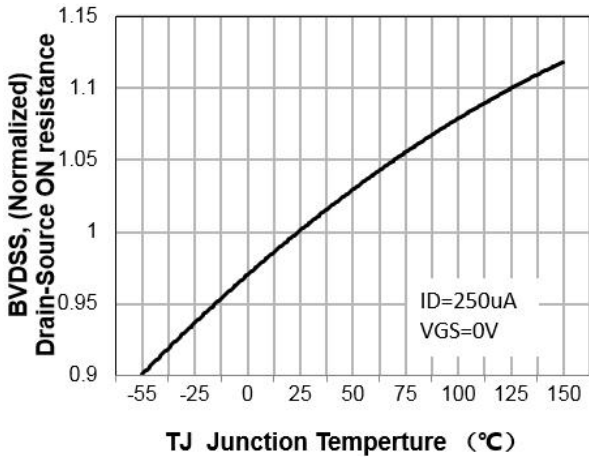


Figure 7. Breakdown Voltage Variation vs Temperature

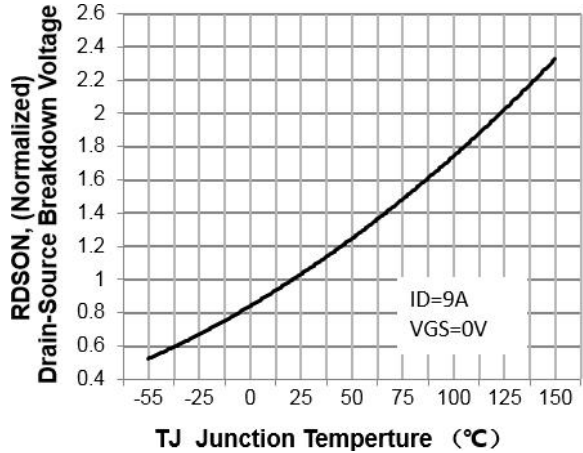


Figure 8. On-Resistance Variation vs Temperature

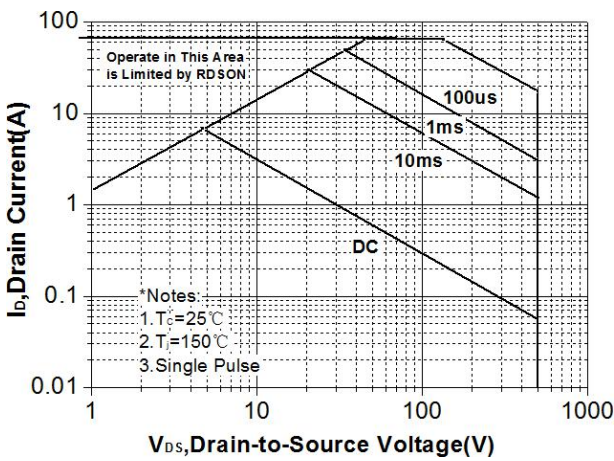


Figure 9. Maximum Safe Operating Area

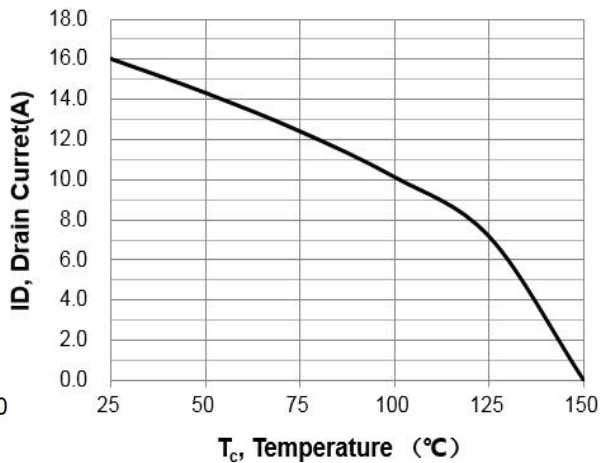


Figure 10. Maximum Drain Current vs Case Temperature

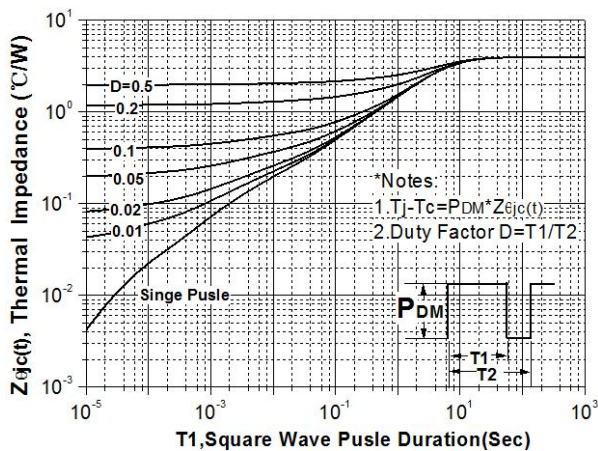
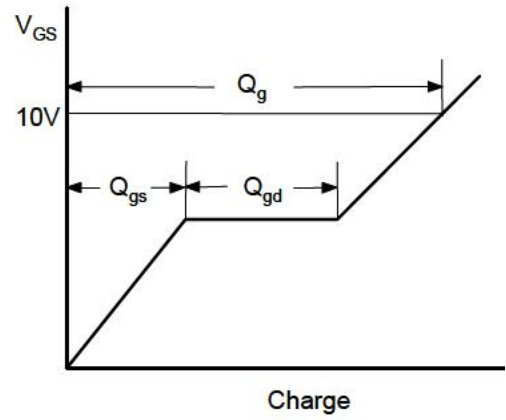
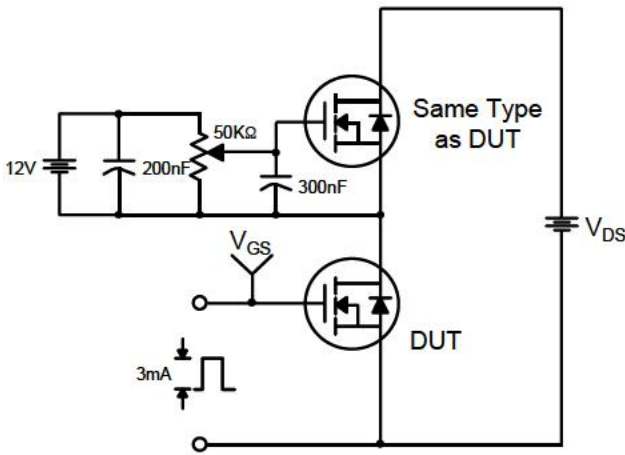


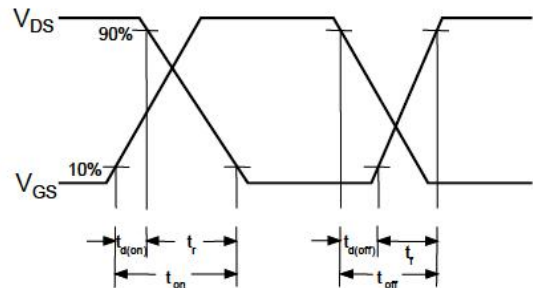
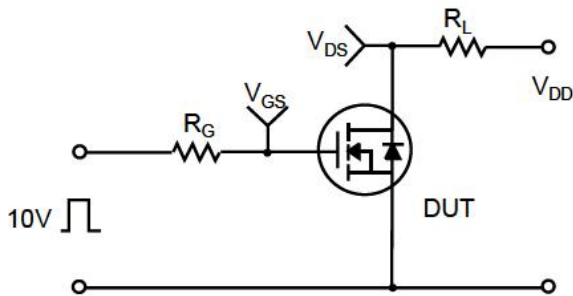
Figure 11. Transient Thermal Response Curve



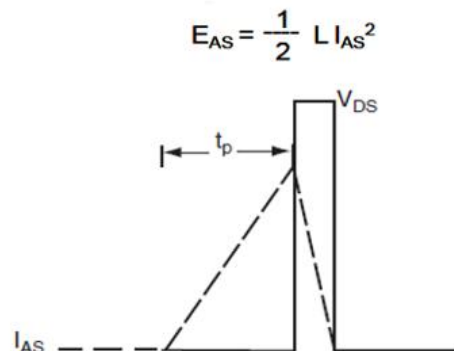
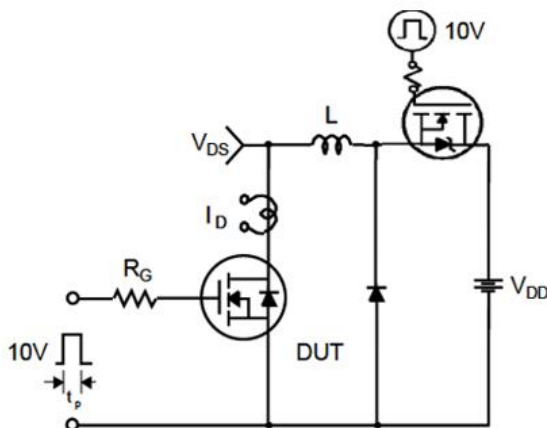
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms

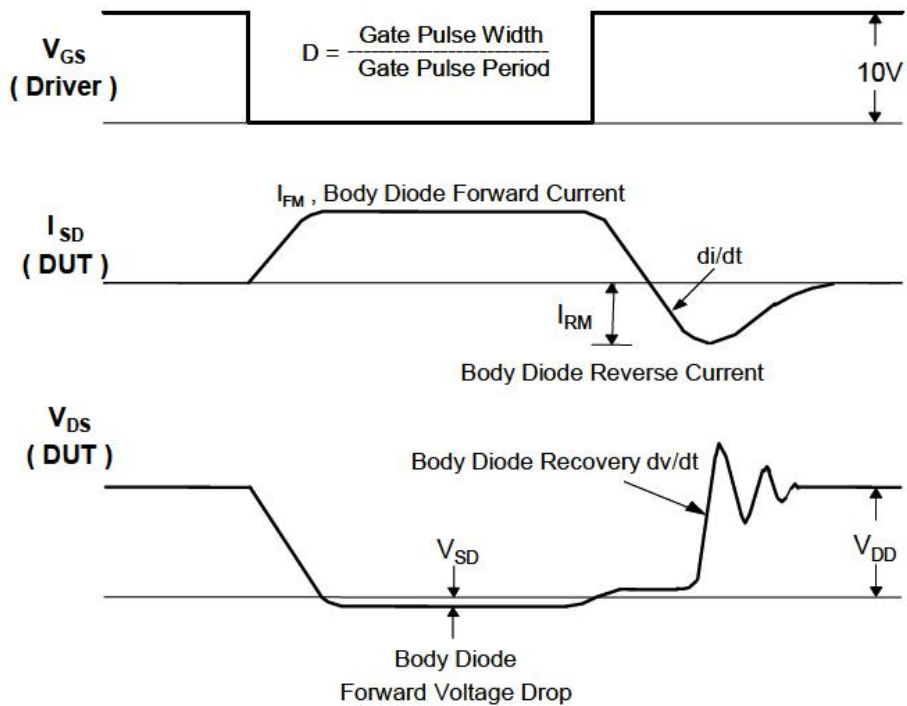
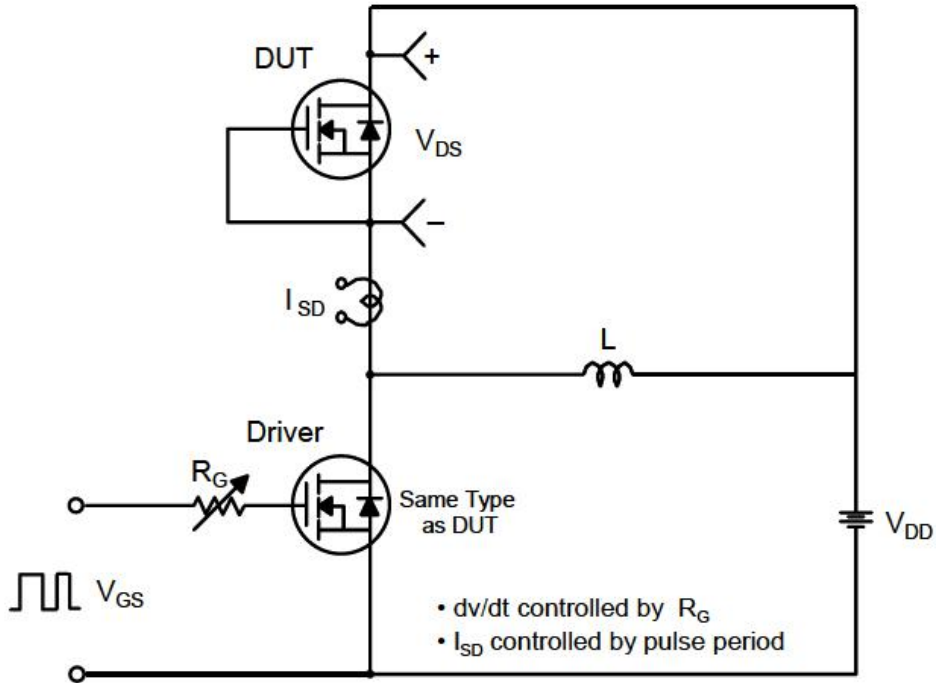


### Unclamped Inductive Switching Test Circuit & Waveforms





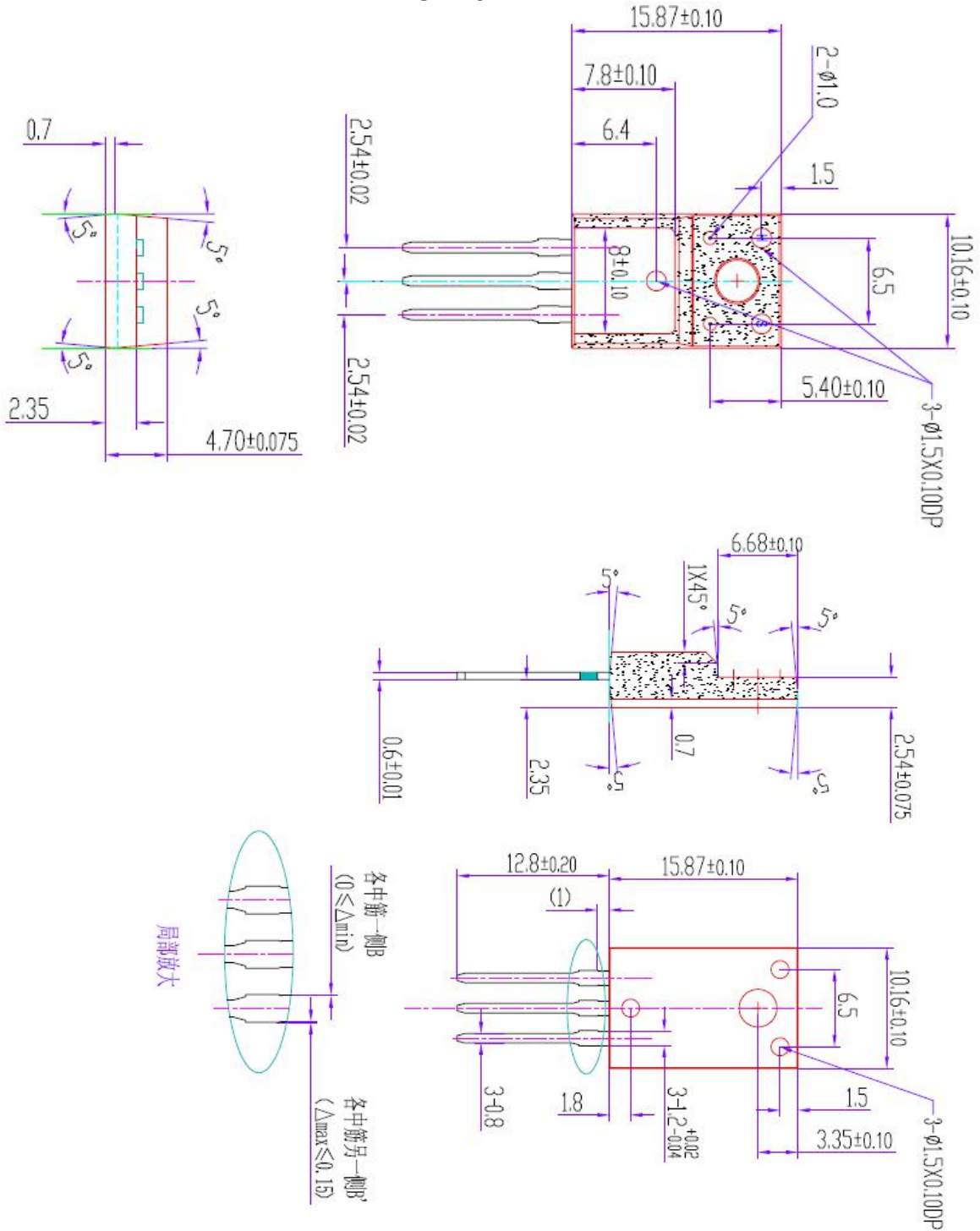
### Peak Diode Recovery dv/dt Test Circuit & Waveforms





### Package Dimensions

#### TO220F





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