



- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

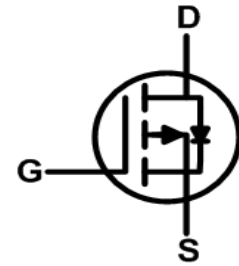
### General Description

The WLB4409 is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

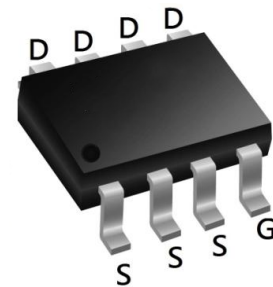
The WLB4409 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

### Product Summary

<b>BVDSS</b>	<b>RDSON</b>	<b>ID</b>
-30V	6.6mΩ	-18A



### SOP8 Pin Configuration



### Absolute Maximum Ratings (T<sub>A</sub>= 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source voltage		<b>V<sub>DS</sub></b>	-30	V
Gate-Source voltage		<b>V<sub>GS</sub></b>	±20	V
Continuous Drain Current	T <sub>A</sub> =25°C	<b>I<sub>D</sub></b>	-18	A
	T <sub>A</sub> =100°C		-8.8	
Pulsed Drain Current <sup>1</sup>		<b>I<sub>DM</sub></b>	-53	A
Single Pulse Avalanche Energy <sup>2</sup>		<b>EAS</b>	80	mJ
Total Power Dissipation	T <sub>A</sub> =25°C	<b>P<sub>D</sub></b>	3	W
Operating Junction and Storage Temperature Range		<b>T<sub>J</sub>, T<sub>STG</sub></b>	-55 to 150	°C

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>3</sup>	<b>R<sub>θJA</sub></b>	41.6	°C/W



**Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
<b>Static Characteristics</b>							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-30	-	-	V	
Gate-body Leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V	T <sub>J</sub> =25°C	-	-	-1	μA
			T <sub>J</sub> =100°C	-	-	-100	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-1.0	-	-2.5	V	
Drain-Source On-Resistance <sup>4</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -12A	-	6.6	9.8	mΩ	
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -10A	-	8.8	14		
Forward Transconductance <sup>4</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -10A	-	50	-	S	
<b>Dynamic Characteristics<sup>5</sup></b>							
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, f = 1MHz	-	3100	-	pF	
Output Capacitance	C <sub>oss</sub>		-	430	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	358	-		
Gate Resistance	R <sub>g</sub>	f=1MHz	-	9.5	-	Ω	
<b>Switching Characteristics<sup>5</sup></b>							
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -15V I <sub>D</sub> = -12A	-	35	-	nC	
Gate-Source Charge	Q <sub>gs</sub>		-	9.9	-		
Gate-Drain Charge	Q <sub>gd</sub>		-	10.5	-		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = -10V, V <sub>DD</sub> = -15V R <sub>G</sub> = 3Ω, I <sub>D</sub> = -12A	-	10.8	-	ns	
Rise Time	t <sub>r</sub>		-	13.2	-		
Turn-Off Delay Time	t <sub>d(off)</sub>		-	73	-		
Fall Time	t <sub>f</sub>		-	35	-		
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -12A, dI <sub>F</sub> /dt = 100A/μs	-	25	-	ns	
Reverse Recovery Charge	Q <sub>rr</sub>		-	10	-	nC	
<b>Drain-source body diode Characteristics</b>							
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	I <sub>S</sub> = -1A, V <sub>GS</sub> = 0V	-	-	-1.2	V	
Continuous Source Current	I <sub>S</sub>	T <sub>A</sub> =25°C	-	-	-14	A	

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>= -25V, V<sub>GS</sub>= -10V, L=0.1mH, I<sub>AS</sub>= -40A.
3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.



### Typical Characteristics

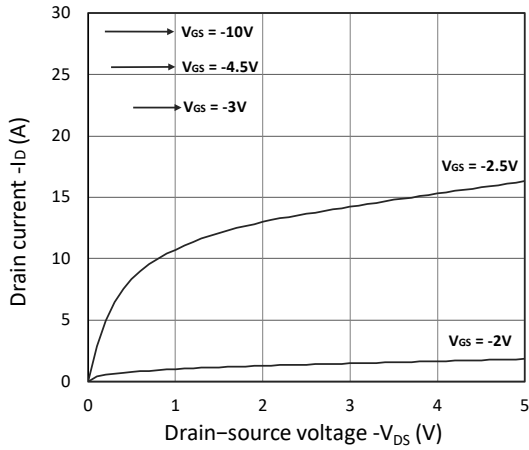


Figure 1. Output Characteristics

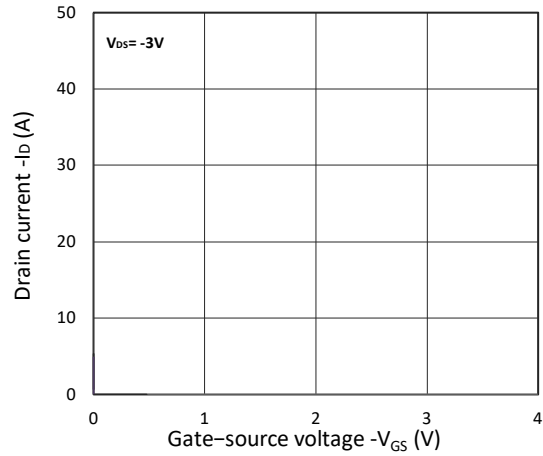


Figure 2. Transfer Characteristics

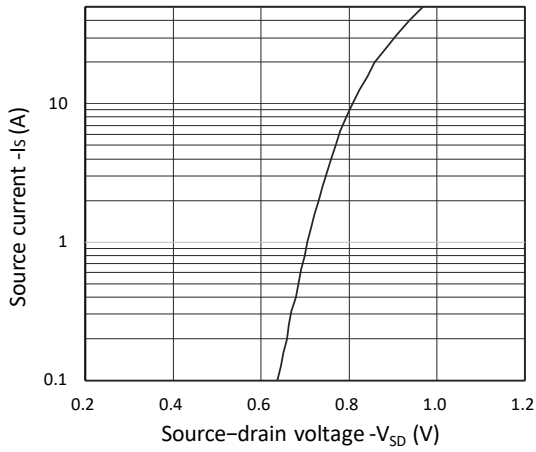


Figure 3. Forward Characteristics of Reverse

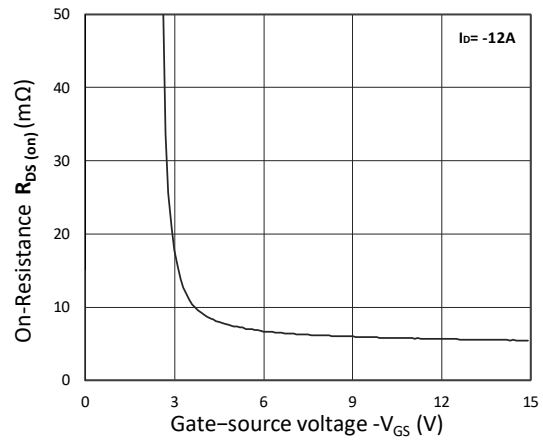


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

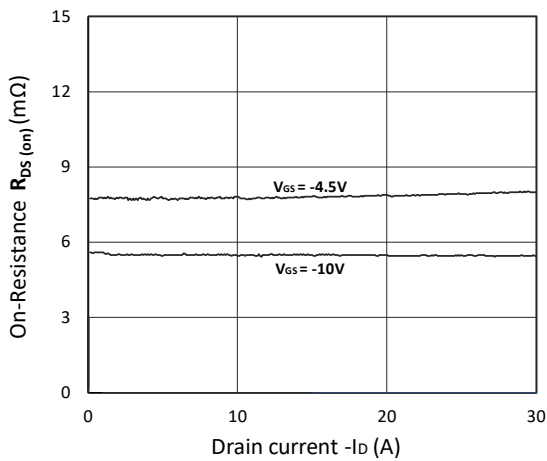


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

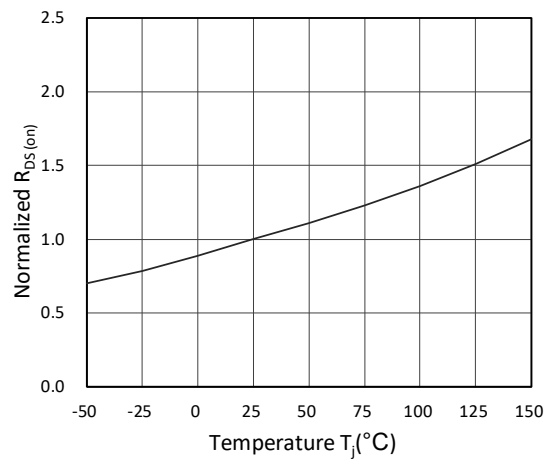


Figure 6. Normalized  $R_{DS(ON)}$  vs. Temperature

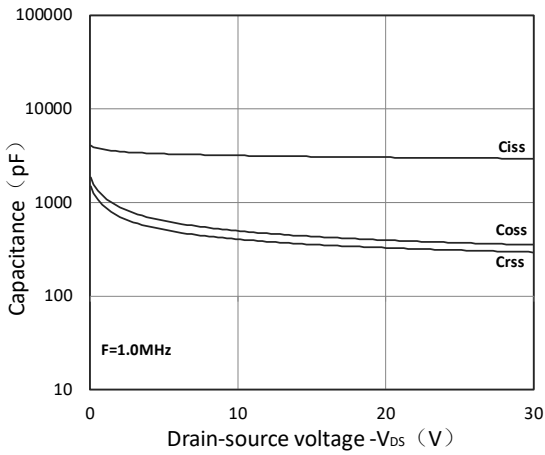


Figure 7. Capacitance Characteristics

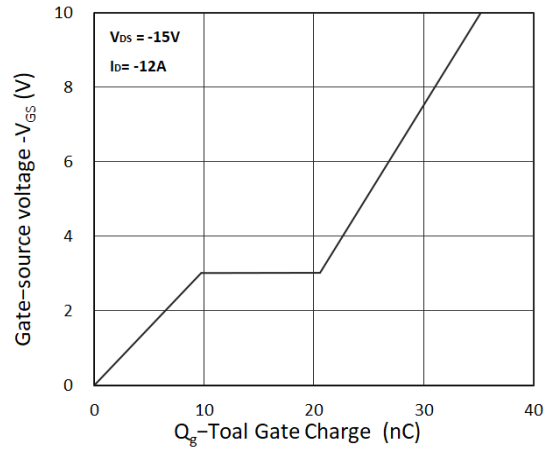


Figure 8. Gate Charge Characteristics

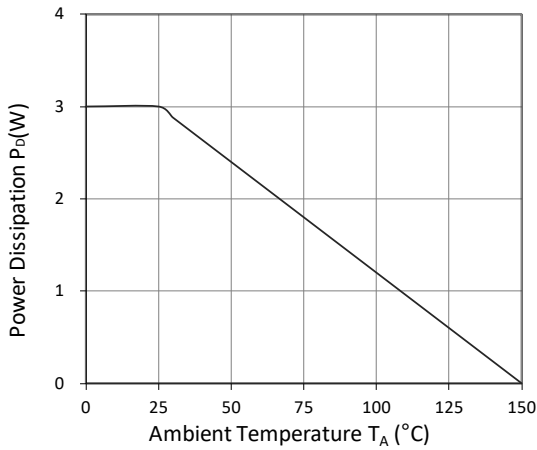


Figure 9. Power Dissipation

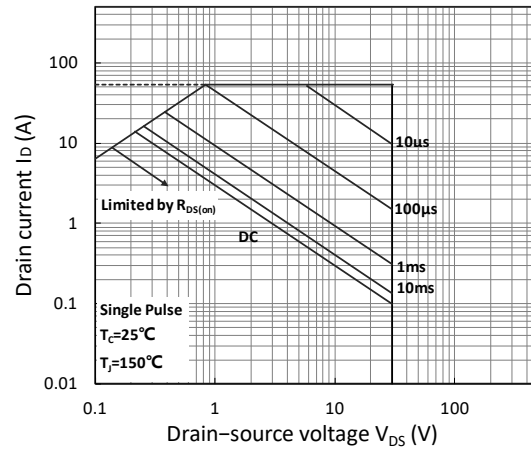


Figure 10. Safe Operating Area

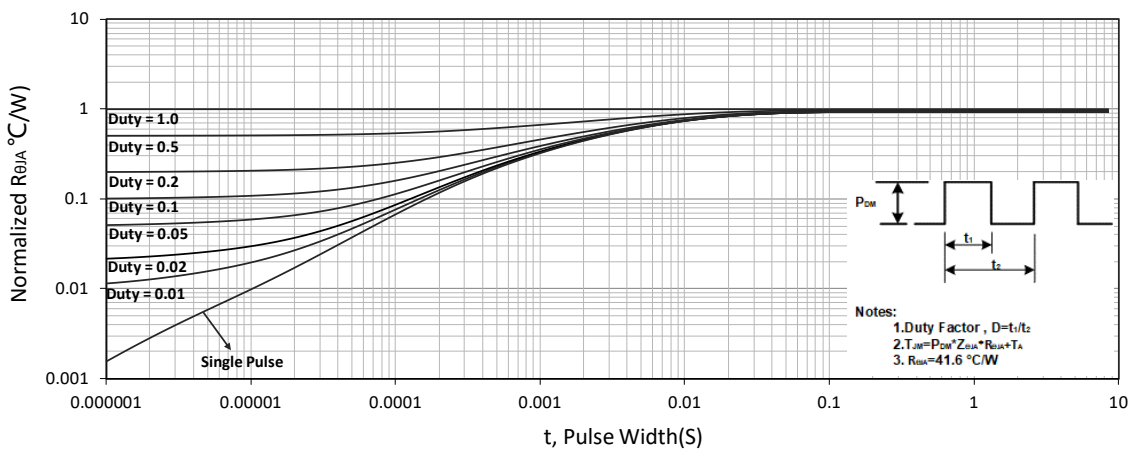


Figure 11. Normalized Maximum Transient Thermal Impedance



Test Circuit

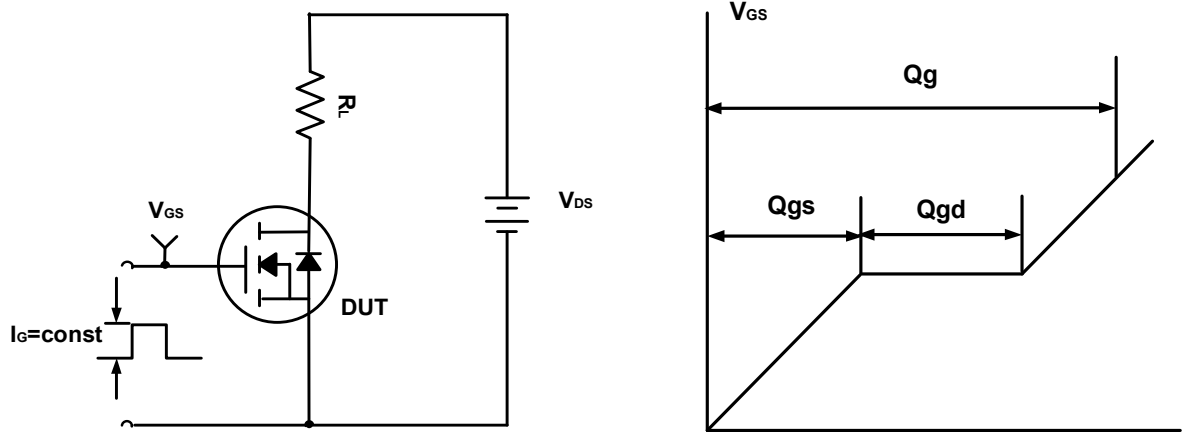


Figure A. Gate Charge Test Circuit & Waveforms

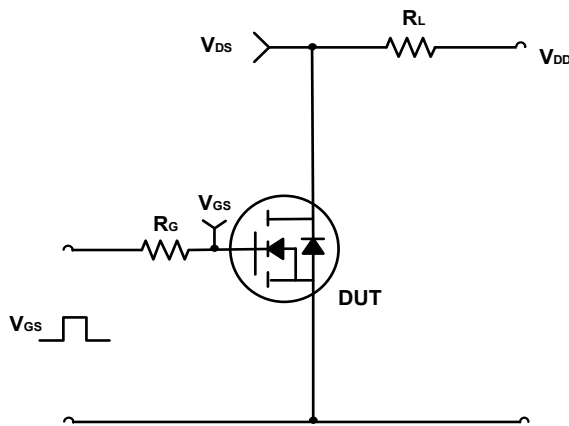


Figure B. Switching Test Circuit & Waveforms

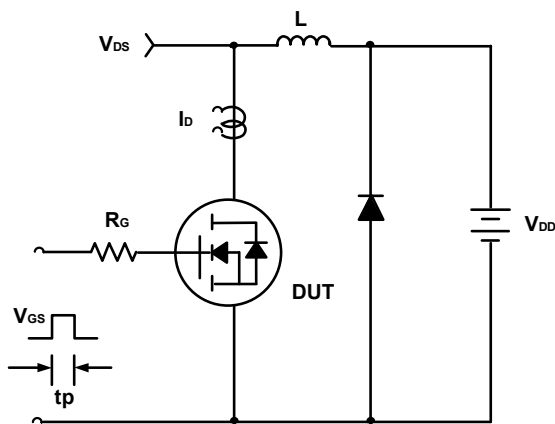
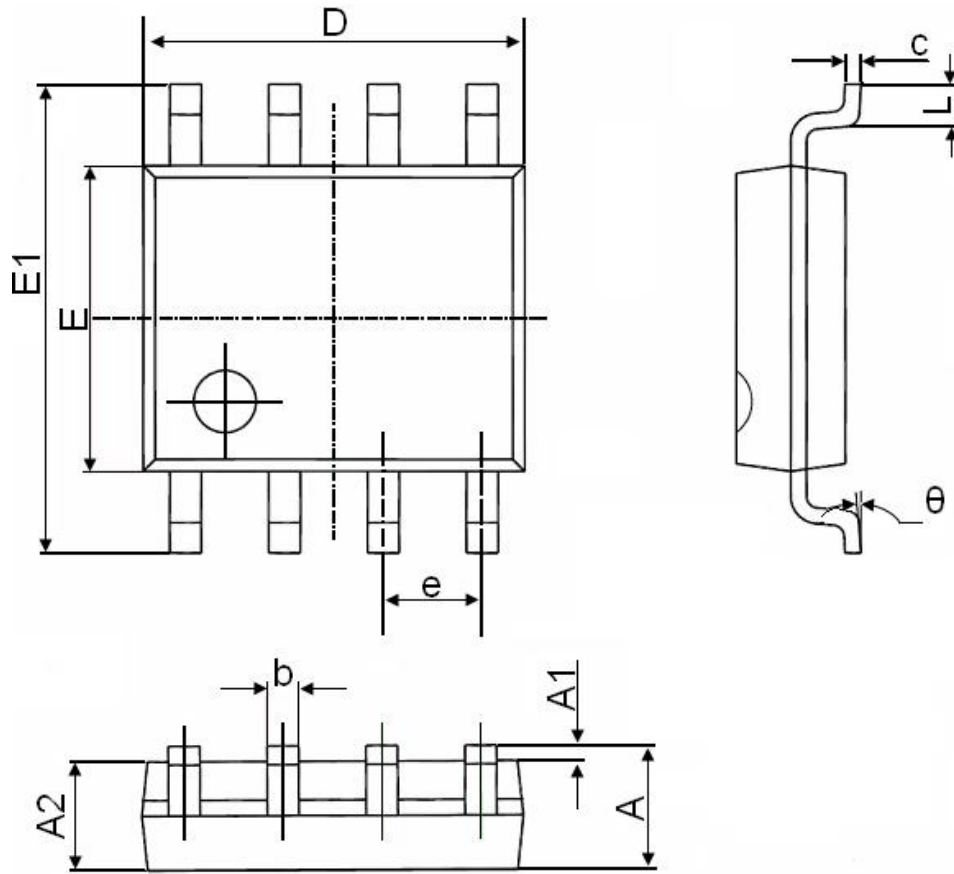


Figure C. Unclamped Inductive Switching Circuit & Waveforms



SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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