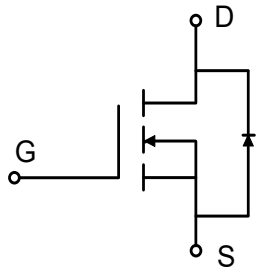
WLP90N20T

N-Ch 200V Fast Switching MOSFETs

- ★ Super Low Gate Charge
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

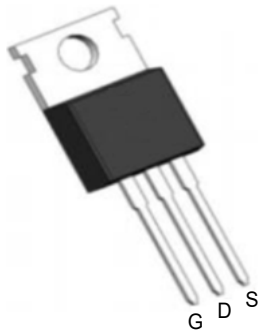


Description

The WLP90N20T is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WLP90N20T meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

TO220 Pin Configuration



Product Summary

BVDSS	RDSON	ID
200V	23 mΩ	90A

Absolute Maximum Ratings (TC= 25℃ unless otherwise specified):

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
VDSS	Drain-Source Voltage		200	V
VGSS	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		175	°C
TSTG	Storage Temperature Range		-55 to 175	°C
IS	Source Current-Continuous(Body Diode)	Tc=25°C	90	A
Mounted on Large Heat Sink				
IDM	Pulsed Drain Current *	Tc=25°C	360	A
ID	Continuous Drain Current	Tc=25°C	90	A
		Tc=100°C	64	A
PD	Maximum Power Dissipation	Tc=25°C	375	W
		Tc=100°C	187.5	W
RθJC	Thermal Resistance, Junction-to-Case		0.4	°C/W
RθJA	Thermal Resistance, Junction-to-Ambient **		40	°C/W
EAS	Single Pulsed-Avalanche Energy ***	L=0.5mH	833	mJ

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_{DS}=250μA	200	-		V
I_{DSS}	Drain-to-Source Leakage Current	V_{DS}=200V,V_{GS}=0V	-	-	1	μA
		T_J=55°C	-	-	5	μA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	2.0	3.0	4.0	V
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$*	Drain-Source On-State Resistance	V_{GS}=10V,I_{DS}=45A		23	25	mΩ
Diode Characteristics						
V_{SD}*	Diode Forward Voltage	I_{SD}=45A,V_{GS}=0V	-	0.85	1.2	V
t_{rr}	Reverse Recovery Time	I_{SD}=45A,dI_{SD}/dt=100A/μs	-	80	-	ns
Q_{rr}	Reverse Recovery Charge		-	160	-	nC

Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY1920 Min	HY1920 Typ.	HY1920 Max	Unit
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V, F=1MHz	-	3.4	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V,	-	5871	-	pF
C_{oss}	Output Capacitance	V_{DS}=25V,	-	392	-	
C_{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz	-	165	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=100V,R_G=4Ω, I_{DS}=45A,V_{GS}=10V	-	29	-	ns
T_r	Turn-on Rise Time		-	45	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	22	-	
T_f	Turn-off Fall Time		-	41	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	V_{DS} =100V, V_{GS}=10V, I_D=30A	-	130	-	nC
Q_{gs}	Gate-Source Charge		-	22	-	
Q_{gd}	Gate-Drain Charge		-	38	-	

[a1]: Repetitive rating; pulse width limited by maximum junction temperature

Typical Operating Characteristic

Figure 1: Power Dissipation

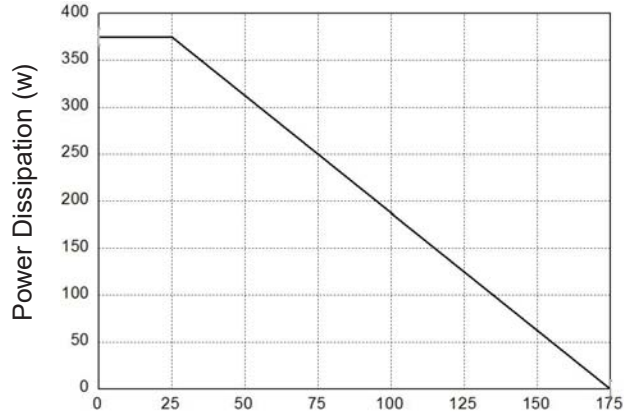


Figure 2: Drain Current

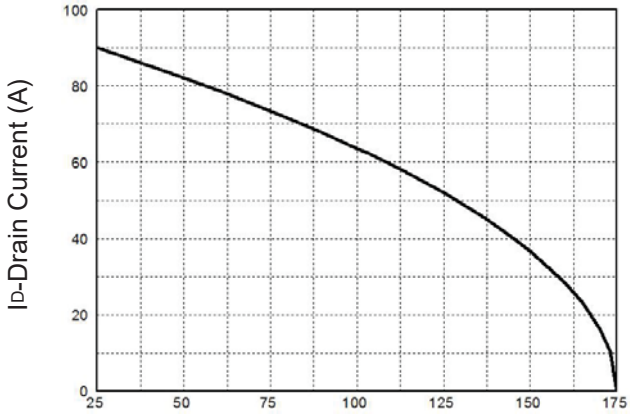


Figure 3: Safe Operation Area

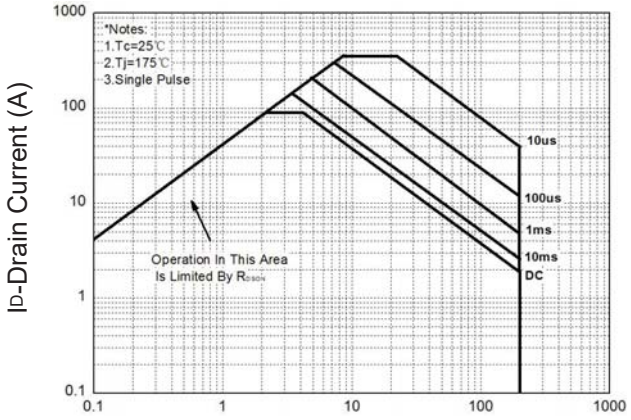


Figure 4: Thermal Transient Impedance

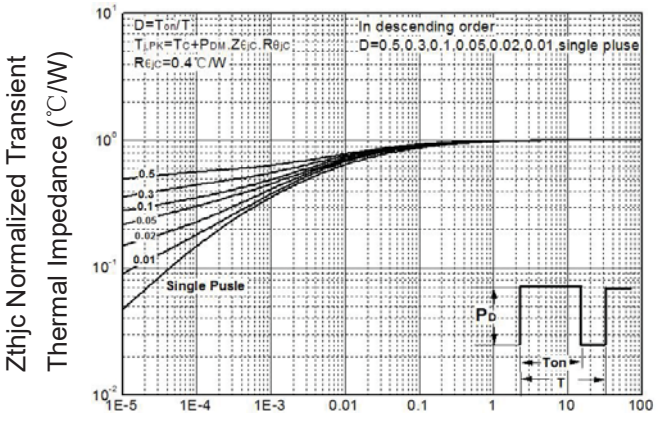


Figure 5: Output Characteristics

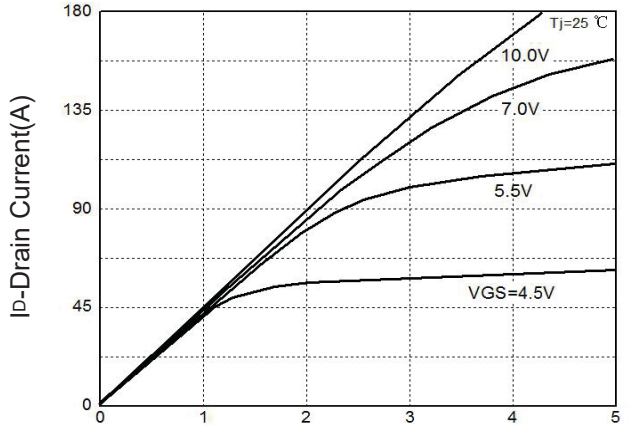
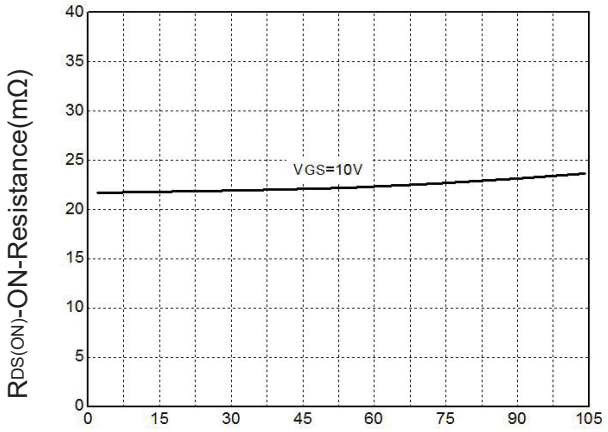
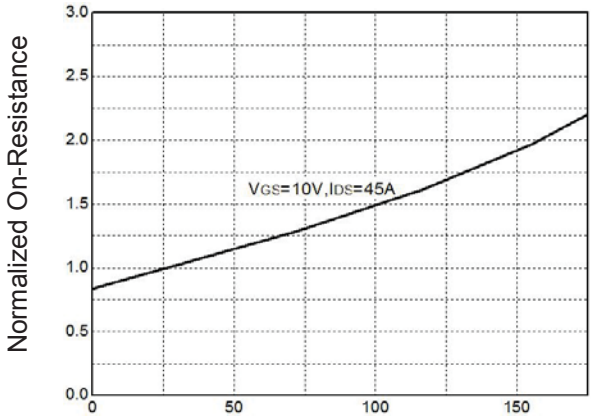


Figure 6: Drain-Source On Resistance



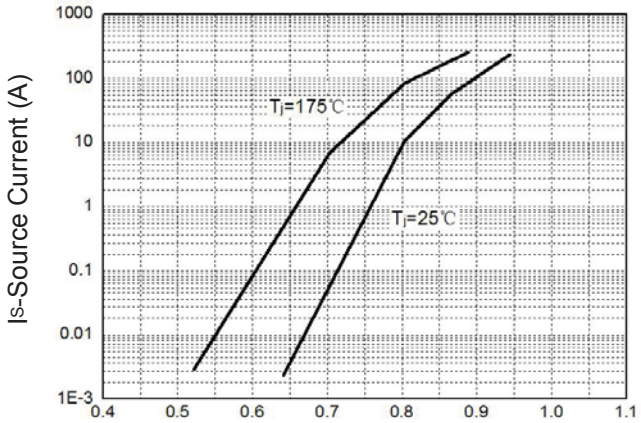
Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature



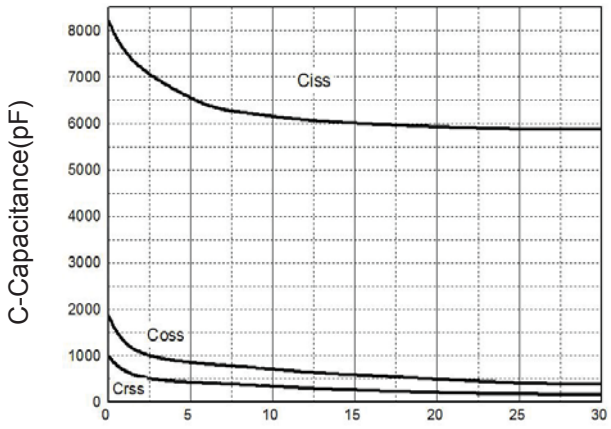
Tj-Junction Temperature (℃)

Figure 8: Source-Drain Diode Forward



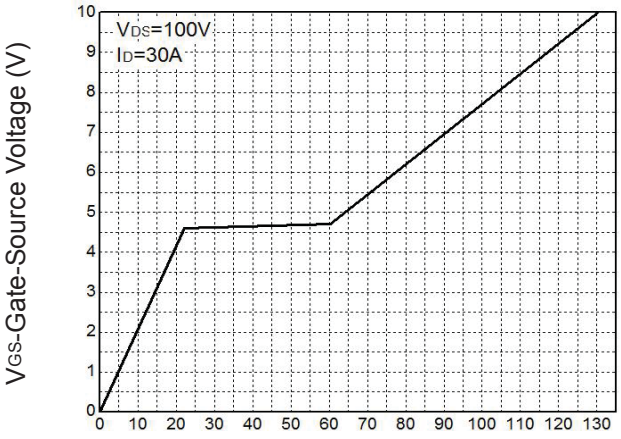
VSD, Source-Drain Voltage(V)

Figure 9: Capacitance Characteristics



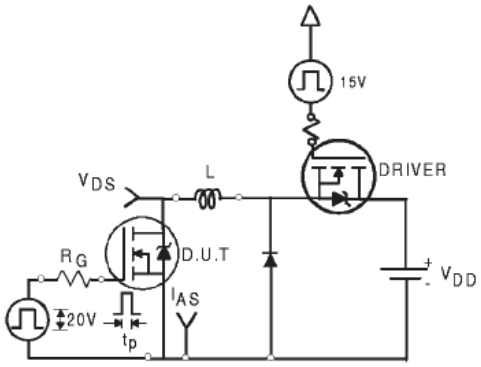
VDS-Drain-Source Voltage (V)

Figure 10: Gate Charge Characteristics

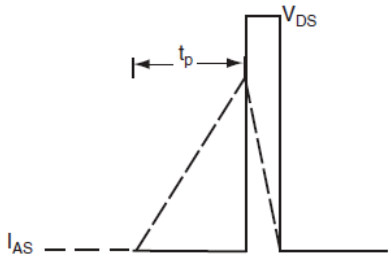


QG-Gate Charge (nC)

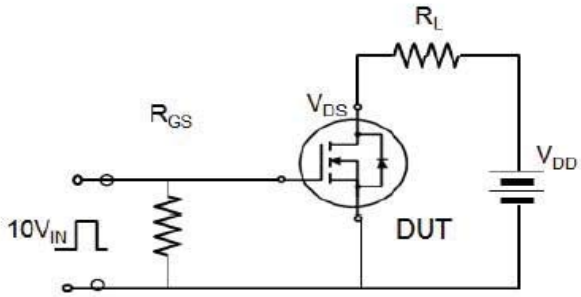
Avalanche Test Circuit

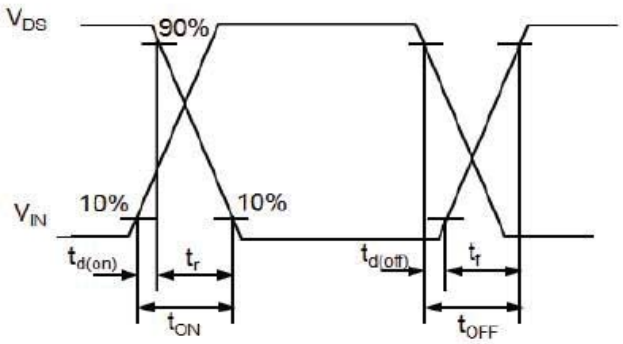


$$E_{AS} = \frac{1}{2} L I_{AS}^2$$

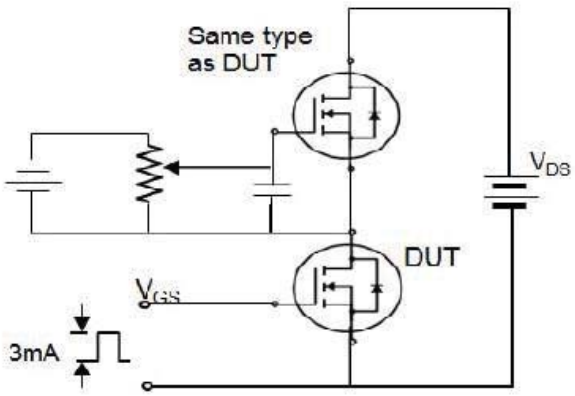


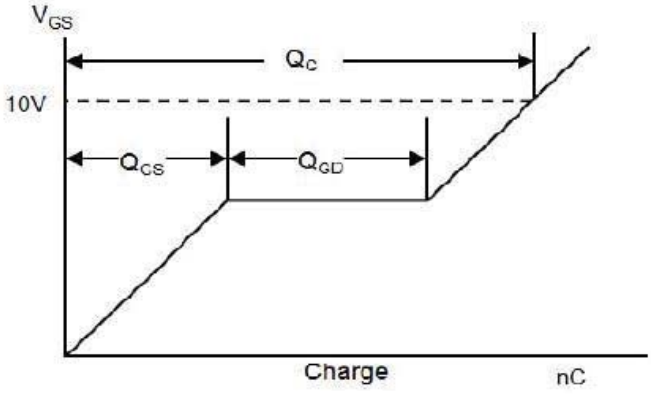
Switching Time Test Circuit



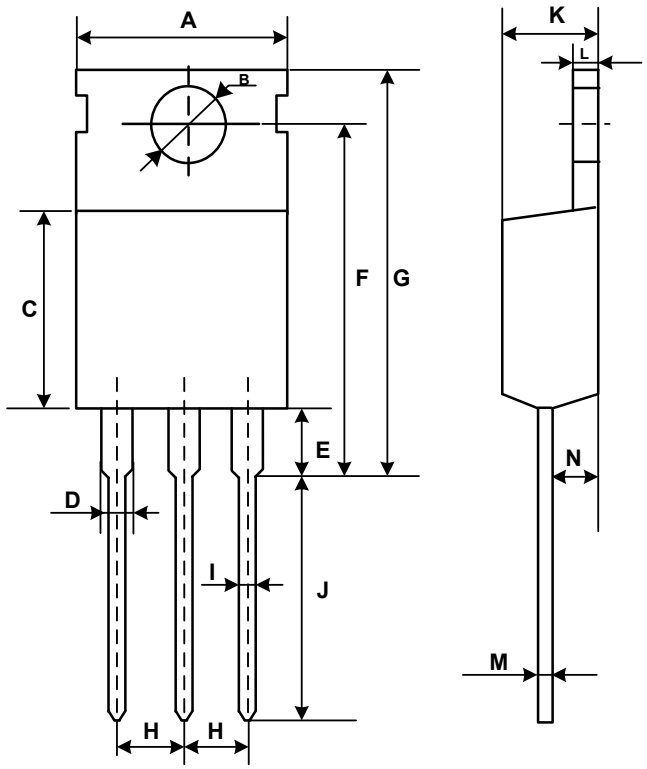


Gate Charge Test Circuit





Mechanical Dimensions for TO-220



OMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	9.70	10.30
B	3.40	3.80
C	8.80	9.40
D	1.17	1.47
E	2.60	3.50
F	15.10	16.70
G	19.55MAX	
H	2.54REF	
I	0.70	0.95
J	9.35	11.00
K	4.30	4.77
L	1.20	1.45
M	0.40	0.65
N	2.20	2.60

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