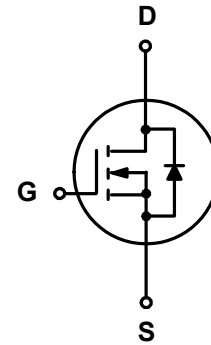


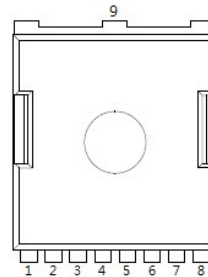


Features:

- Low Intrinsic Capacitances
- Excellent Switching Characteristics
- Extended Safe Operating Area
- Ultra Low Gate Charge: $Q_g=69\text{nC}$ (Typ.)
- $V_{DS}=200\text{V}$, $I_D=105\text{A}$
- $R_{ds(on)}:8.8\text{m}\Omega$ (Typ.) @ $V_G=10\text{V}$
- 100% Avalanche Tested



TOLL-8L



1 Gate(G)
2,3,4,5,6,7,8 Source(S)
9 Drain(D)

Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	200	V
Continuous drain current $T_C = 25^\circ\text{C}$ (Silicon limit) $T_C = 25^\circ\text{C}$ (Package limit) $T_C = 100^\circ\text{C}$ (Silicon limit)	I_D	105 160 67	A
Pulsed drain current ($T_C = 25^\circ\text{C}$, t_p limited by T_{jmax})	$I_{D\ pulse}$	420	A
Avalanche energy, single pulse ($L=0.5\text{mH}$, $R_g=25\ \Omega$) ^[1]	$E_{AS(\text{Note } 1)}$	306	mJ
Gate-Source voltage	V_{GS}	± 20	V
Power dissipation ($T_C = 25^\circ\text{C}$)	P_{tot}	278	W
Operating junction and storage temperature	T_j, T_{stg}	-55...+150	$^\circ\text{C}$

※. Notes: 1. EAS is tested at starting $T_j = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $I_{AS} = 35\text{A}$, $V_{gs}=10\text{V}$.

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	R_{thJC}	0.45	$^\circ\text{C}/\text{W}$
Thermal resistance, junction – ambient(min. footprint)	R_{thJA}	62.5	



Static Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	200	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	2	3	4	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	I_{DSS}	-	-	1	μA	$V_{DS}=200V, V_{GS}=0V$ $T_j=25^\circ C$ $T_j=125^\circ C$
Gate-source leakage current	I_{GSS}	-	-	100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	8.8	10.6	m Ω	$V_{GS}=10V, I_D=50A$
Transconductance	g_{fs}	-	93	-	S	$V_{DS}=5V, I_D=50A$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	4819	-	pF	$V_{GS}=0V, V_{DS}=100V,$ $f=1MHz$
Output Capacitance	C_{oss}	-	405	-		
Reverse Transfer Capacitance	C_{rss}	-	23	-		
Gate Total Charge	Q_G	-	69	-	nC	$V_{GS}=10V, V_{DS}=100V,$ $I_D=50A, f=1MHz$
Gate-Source charge	Q_{gs}	-	25	-		
Gate-Drain charge	Q_{gd}	-	16	-		
Turn-on delay time	$t_{d(on)}$	-	16	-	ns	$V_{DS}=100V$ $I_D=50A$ $R_G=2.7\Omega$ $V_{GS}=10V;$
Rise time	t_r	-	82	-		
Turn-off delay time	$t_{d(off)}$	-	55	-		
Fall time	t_f	-	84	-		
Gate resistance	R_G	-	3.5	-	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1MHz$

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	0.85	1.4	V	$V_{GS}=0V, I_{SD}=50A$
Body Diode Reverse Recovery Time	t_{rr}	-	129	-	ns	$I_{SD}=50A, V_{GS}=0V,$ $dI/dt=100A/us;$
Body Diode Reverse Recovery Charge	Q_{rr}	-	752	-	nC	



Typical Performance Characteristics

Fig 1: Output Characteristics

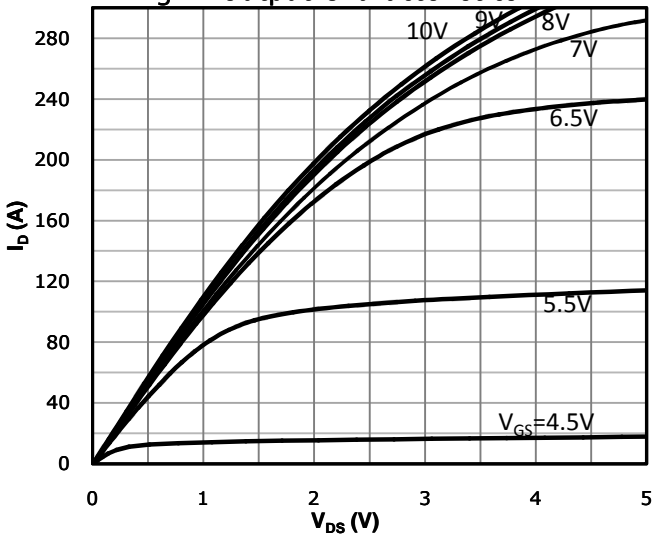


Fig 2: Transfer Characteristics

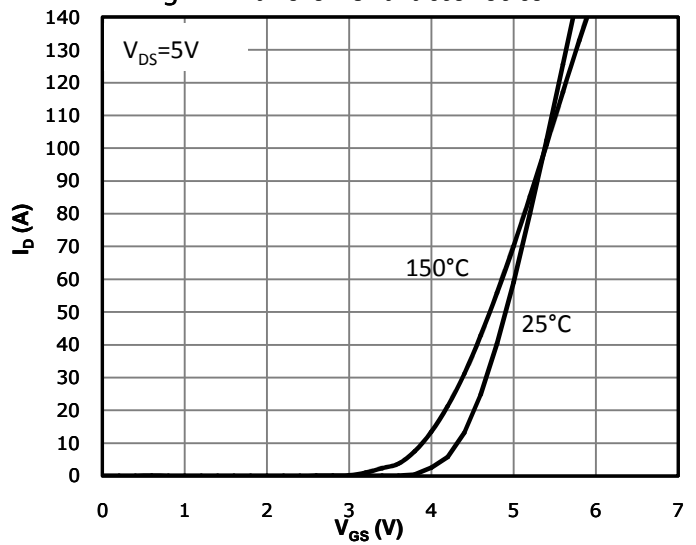


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

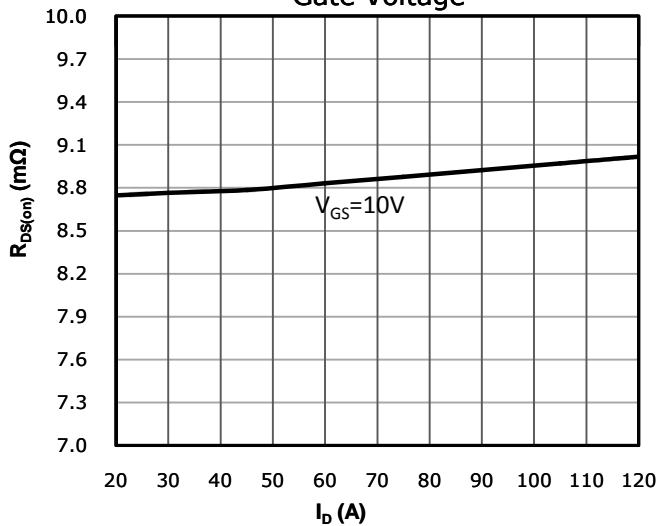


Fig 4: $R_{DS(on)}$ vs Gate Voltage

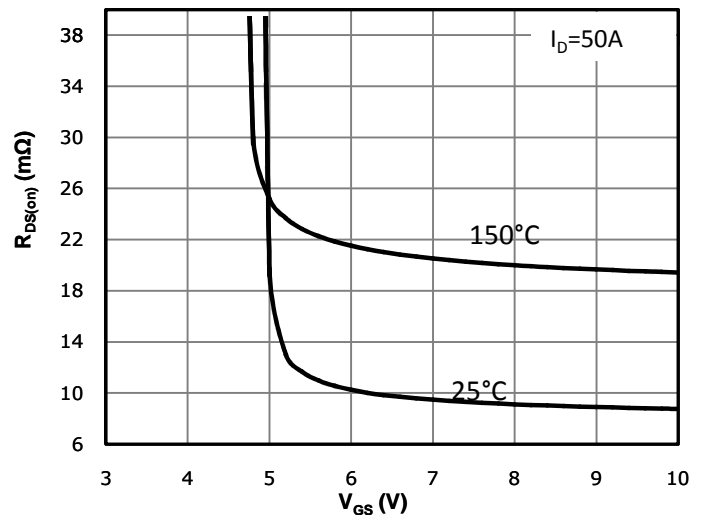


Fig 5: $R_{DS(on)}$ vs. Temperature

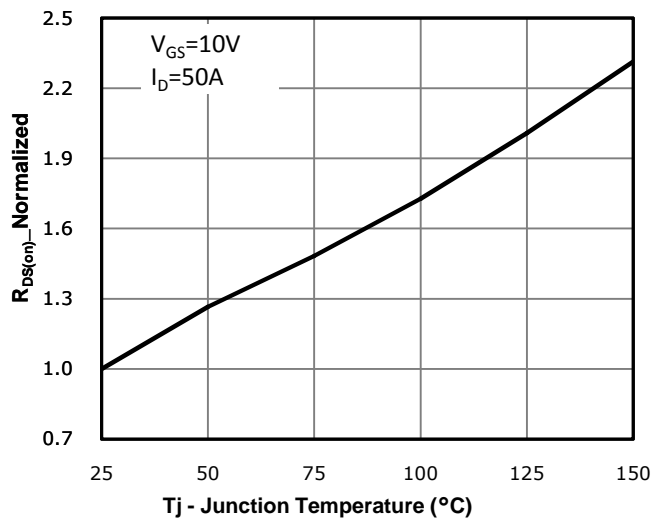
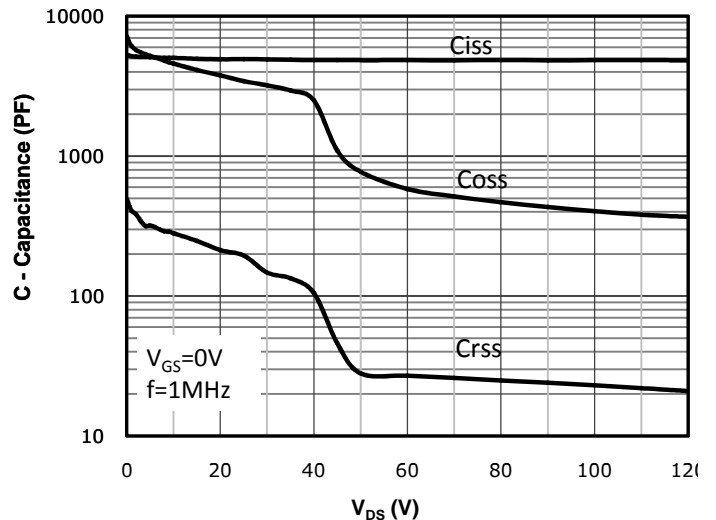


Fig 6: Capacitance Characteristics





Typical Performance Characteristics

Fig 7: Gate Charge Characteristics

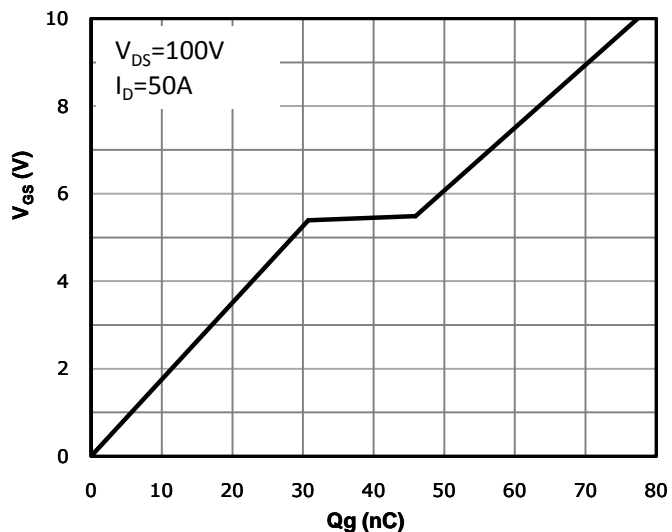


Fig 8: Body-diode Forward Characteristics

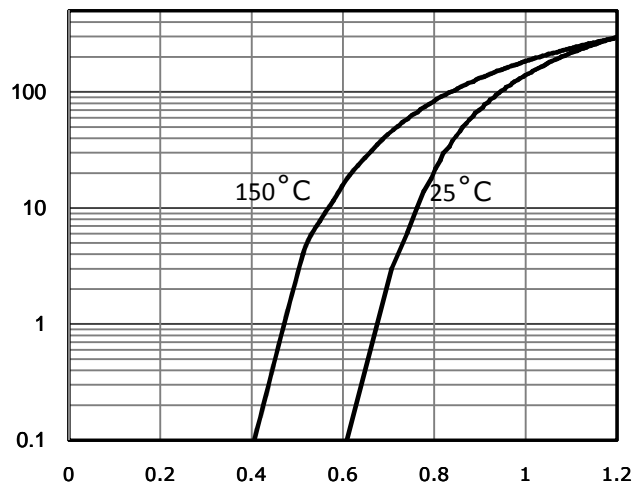


Fig 9: Power Dissipation

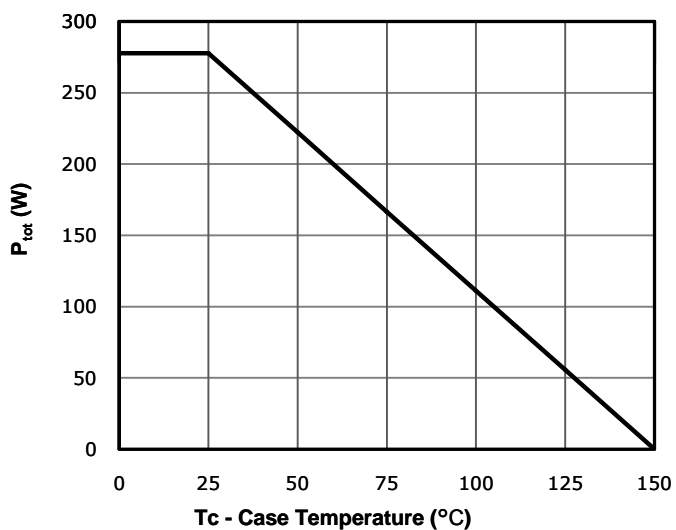


Fig 10: Drain Current Derating

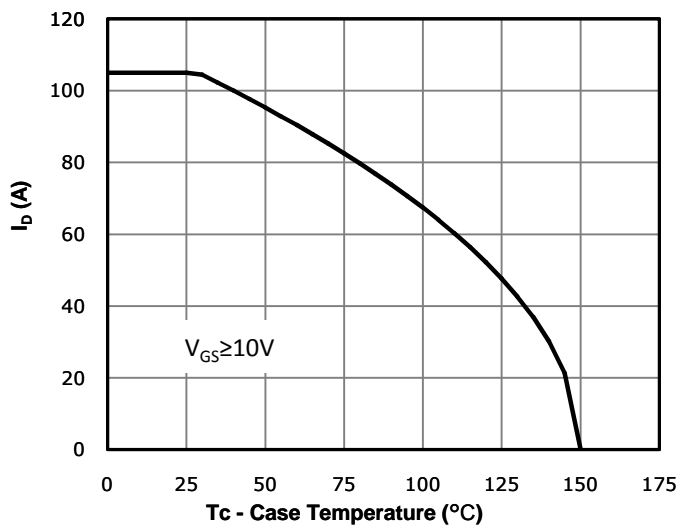
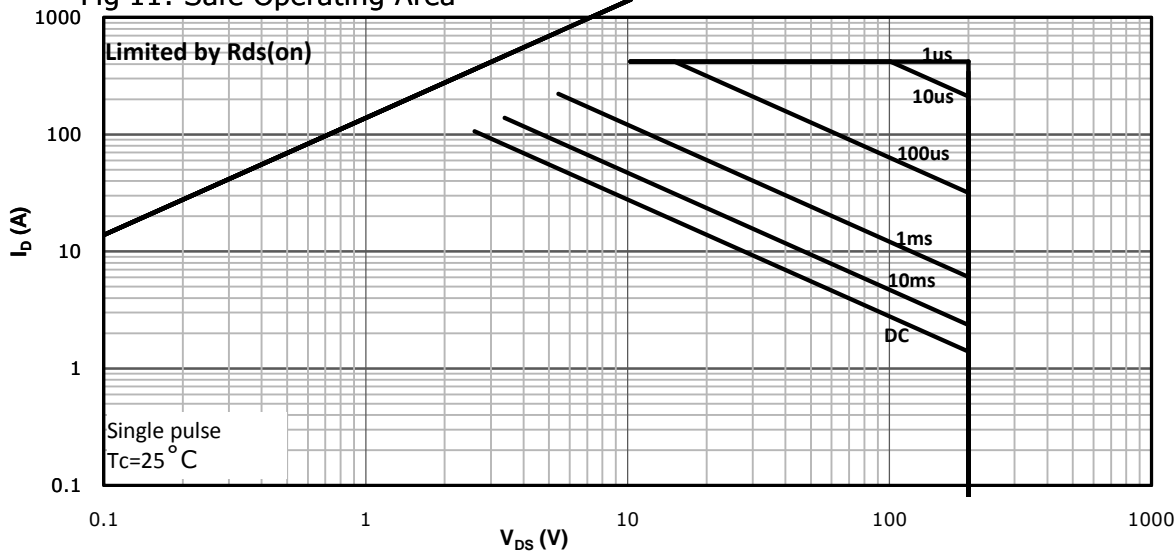


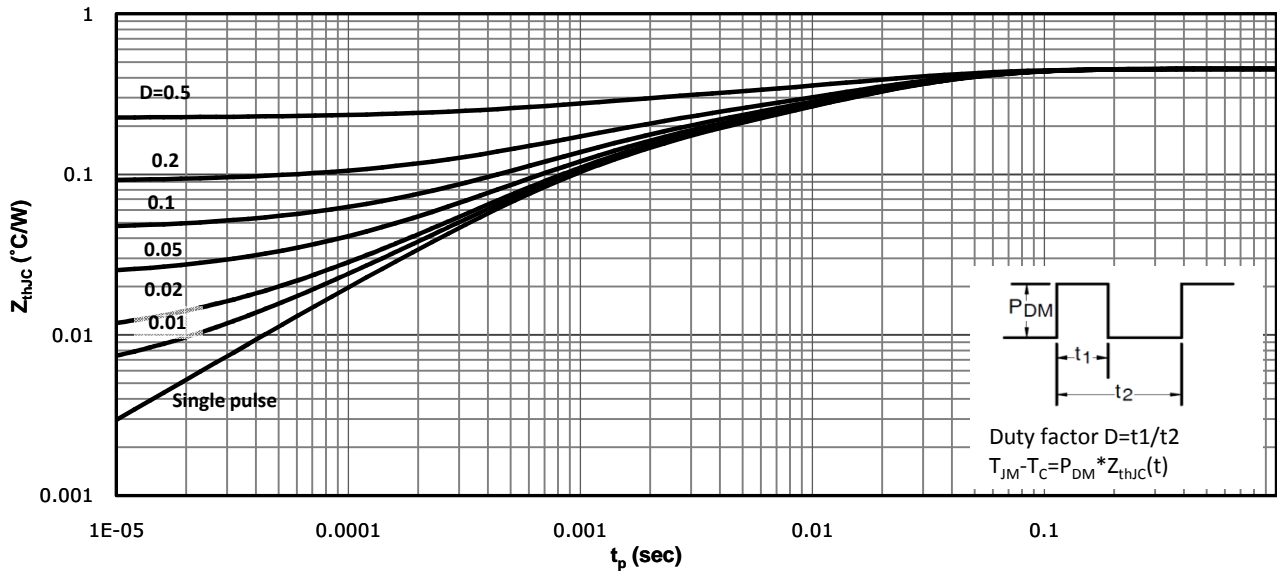
Fig 11: Safe Operating Area





Typical Performance Characteristics

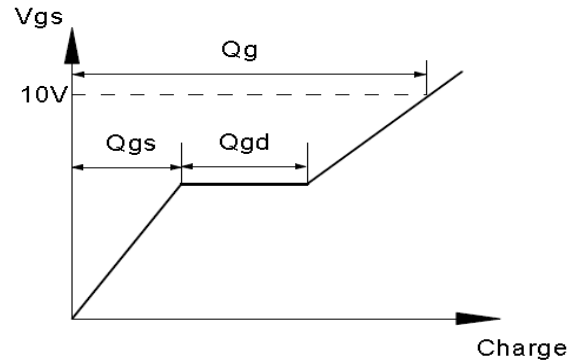
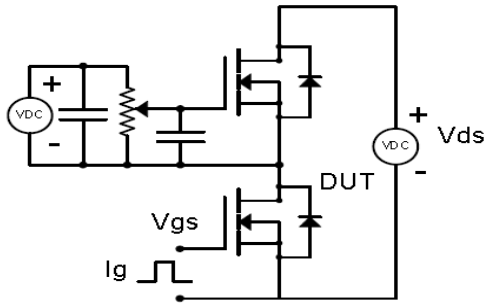
Fig 12: Max. Transient Thermal Impedance



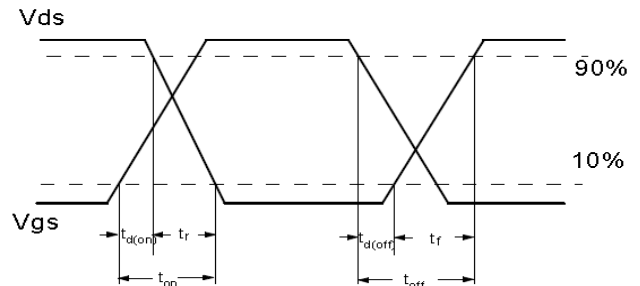
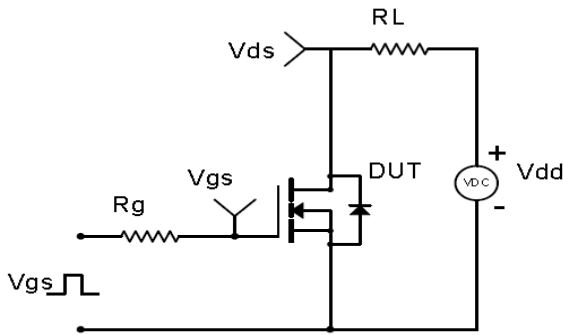


Test Circuit & Waveform

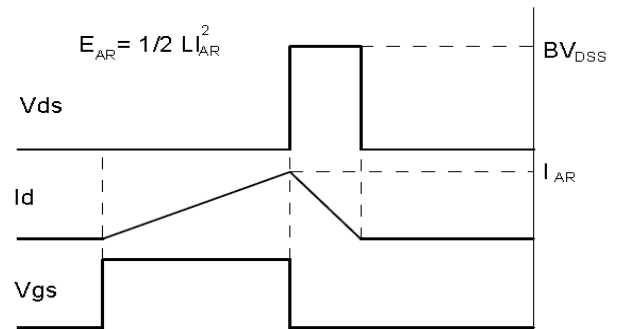
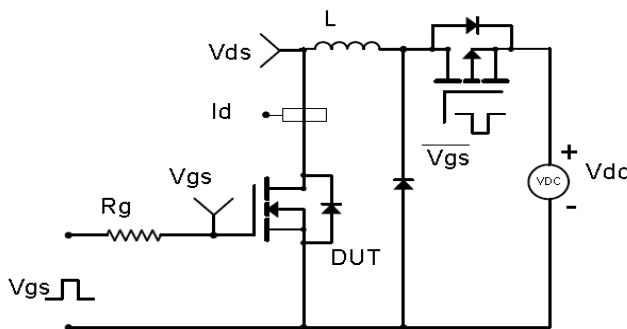
Gate Charge Test Circuit & Waveform



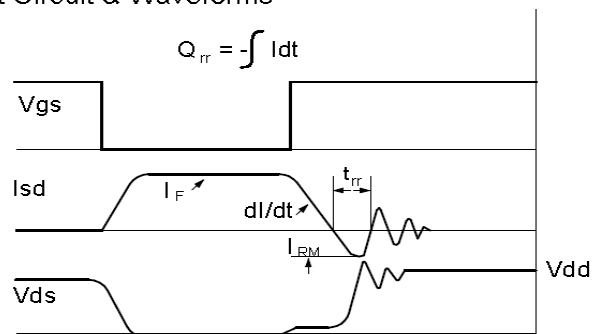
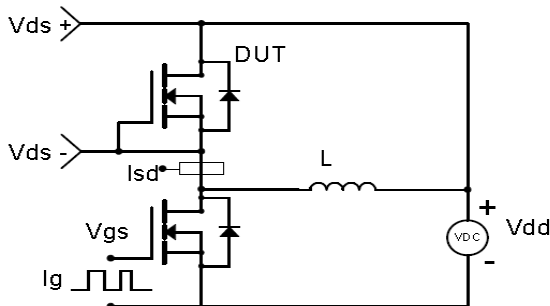
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

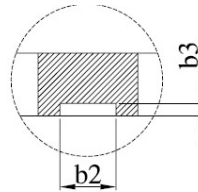
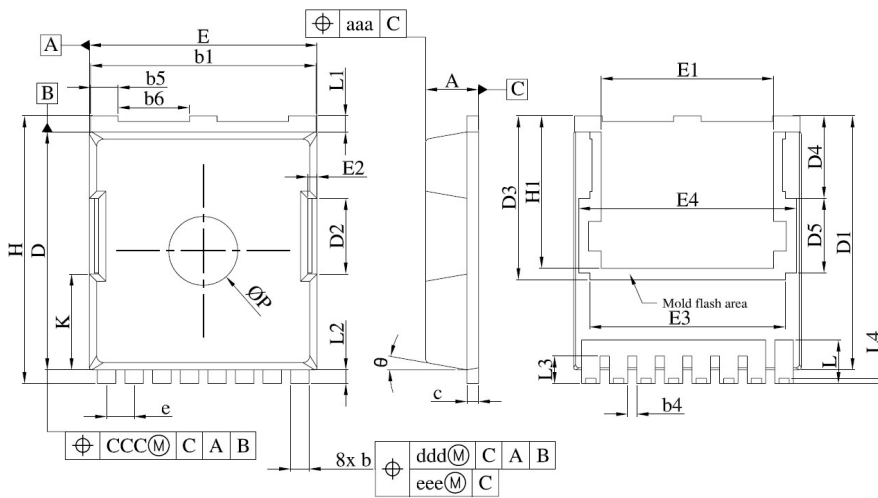




Package Dimension

TOLL-8L

Unit: mm



Symbol	Dimensions In Millimeters		
	MIN.	NOM.	MAX.
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.36	0.45	0.55
b3	0.05	0.10	/
b4	0.30	0.40	0.50
b5	1.10	1.20	1.30
b6	3.00	3.10	3.20
c	0.40	0.50	0.60
D	10.28	10.38	10.55
D1	10.98	11.08	11.18
D2	3.20	3.30	3.40
D3	7.15		
D4	3.59		
D5	3.26		
e	1.10	1.20	1.30
E	9.80	9.90	10.00
E1	7.40	7.50	7.60
E2	0.30	0.40	0.50
E3	8.50		
E4	9.46		
H	11.50	11.68	11.85
H1	6.55	6.65	6.75
K	4.08	4.18	4.28
L	1.60	1.90	2.10
L1	0.50	0.70	0.90
L2	0.50	0.60	0.70
L3	1.00	1.20	1.30
L4	0.13	0.23	0.33
p	2.85	3.00	3.15
θ	10°REF		
aaa	0.20		
ccc	0.20		
ddd	0.25		
eee	0.20		



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